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HSF Property:ROHS or Halogen-Free

COMET DIS

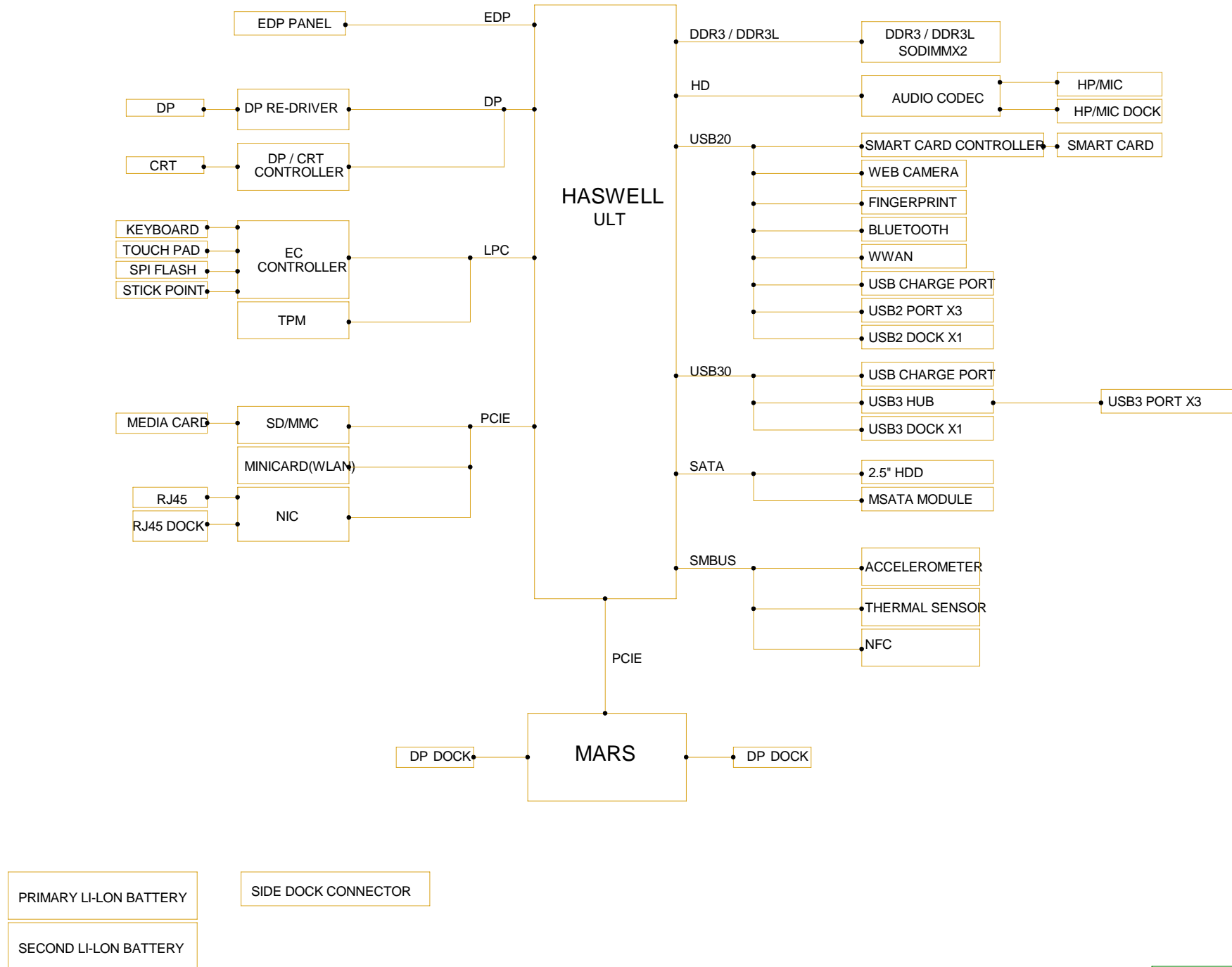
SI2 BUILD

2013.02.27

21-OCT-2002		
DATE	CHANGE NO.	REV

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INVENTEC					
DRAWER	EE	DATE	POWER	DATE	TITLE MODEL, PROJECT, FUNCTION COMET
DESIGN	EDI CHEN	2013/1/8	RAY CHEN	2013/1/8	
CHECK	EDI CHEN	2013/1/8	RAY CHEN	2013/1/8	
RESPONSIBLE	EDI CHEN	2013/1/8	RAY CHEN	2013/1/8	
SIZE=	A3	VER.		AX1	
FILE NAME:	COMET DIS				SIZE
PIN	0000426690101_XXXX				C
					CODE
					CS
					DOC NUMBER
					E310XXXX-0-0
					REV
					X01
					SHEET
					1 of 78



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	131000000-0-0	201
SHEET		1	of 08

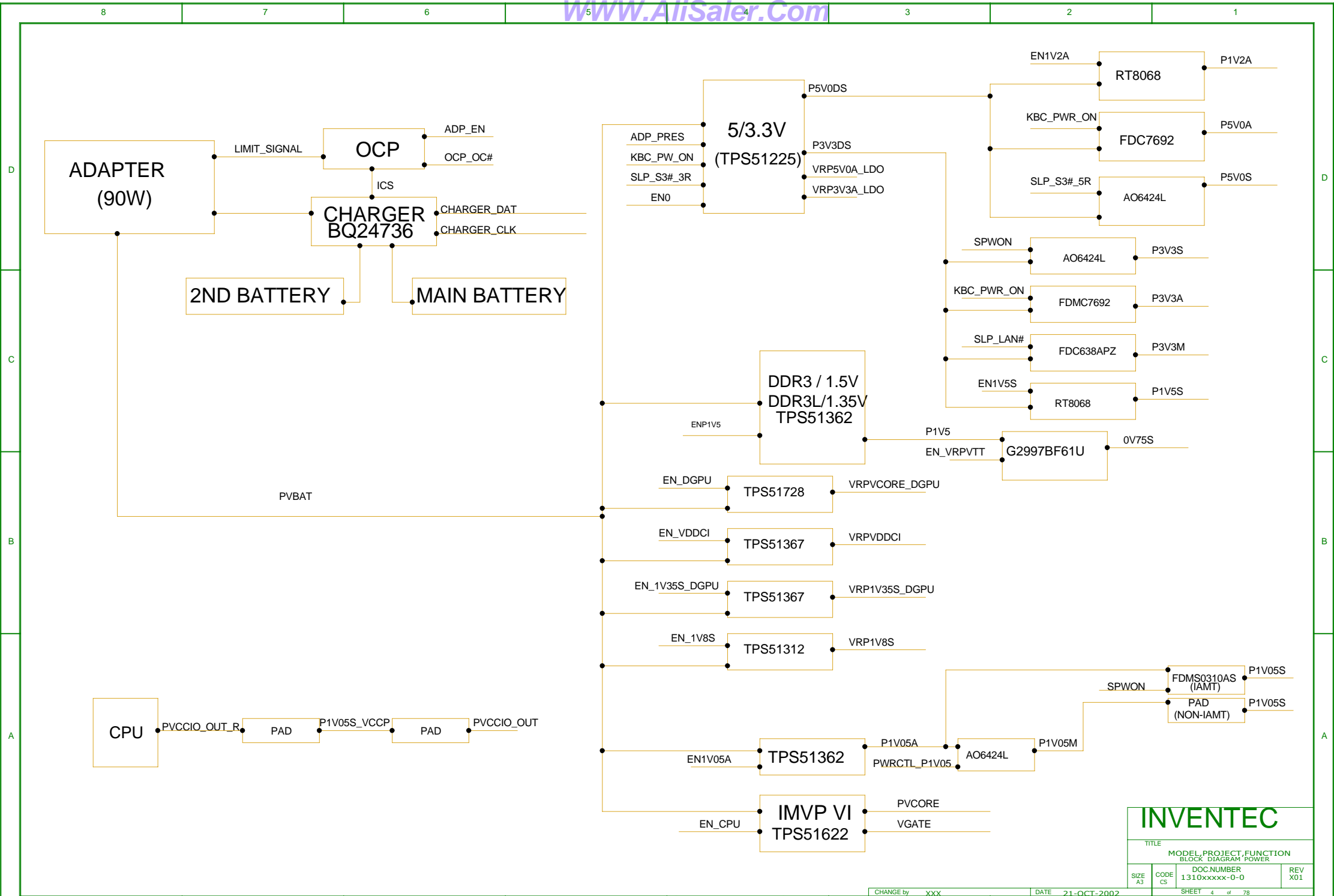
TABLE OF CONTENTS

01.PROJECT NAME	26.XDP & ME CONN.	51.KBC, SPI
02.BLOCK DIAGRAM	27.HASWELL_1 (MISC,JTAG)	52.KEYBOARD
03.TABLE OF CONTENTS	28.HASWELL_2 (LPC,SPI,SMBUS,CLINK,PM)	53.TPM
04.POWER BLOCK DIAGRAM	29.HASWELL_3 (GPIO)	54.LAN
05.SYSTEM POWER(CHARGER)	30.HASWELL_4 (DP,EDP)	55.WLAN/BT SLOT
06.SYSTEM POWER(BATT SELECTOR)	31.HASWELL_5 (DDR)	56.WWAN SLOT, SIM SLOT
07.SYSTEM POWER(OCF)	32.HASWELL_6 (PCIE,USB)	57.DOCKING
08.SYSTEM POWER(P3V3A&P5V0A)	33.HASWELL_7 (RTC,AUDIO,SATA,JTAG)	58.B TO B CONN, POINT STICK CONN
09.P3V3A&P5V0A_CHG PORT	34.HASWELL_8 (CLK)	59.AUDIO CODEC
10.SYSTEM POWER(P1V5)	35.HASWELL_9 (POWER)	60.AUDIO JACK, MIC AMP.
11.SYSTEM POWER(P1V05_M)	36.HASWELL_10 (POWER)	61.CARD READER
12.SYSTEM POWER(P1V05S)	37.HASWELL_11 (GND)	62.BUTTON, LED
13.USB HUB POWER (P1V2A)	38.SYSTEM MEMORY (DIMM0)	63.SMART CARD DAUGHTER BOARD
14.SYSTEM POWER(PVCORE&PVAXG-1)	39.SYSTEM MEMORY (DIMM1)	64.CRT DB WTB CONN
15.SYSTEM POWER(PVCORE&PVAXG-2)	40.EMPTY	65.MIC DB
16.POWER SEQUENCE (SLEEP)	41.EMPTY	66.SCREW
17.DC JACK & BATTERT CONN.	42.DP TO VGA CONVERTER	67.EMPTY
18.HP_OCP	43.DISPLAY PORT	68.MARS-1
19.PVCORE_DGPU	44.LCM & WEBCAM CONN	69.MARS-2
20.PVDDCI	45.SATA HDD, MSATA	70.MARS-3
21.P1V35S_DGPU	46.USB HUB	71.MARS-4
22.P1V8S	47.USB3 PORTS	72.MARS-5
23.PVPCIE	48.USB & CRT DB	73.MARS-6
24.POWER (SEQUENCE)	49.FINGERPRINT READER, NFC	74.MARS-7
25.FAN & THERMAL	50.ACCELEMETER	75.MARS-8
		76.VRAM-1
		77.VRAM-2

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
TABLE OF CONTENTS			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

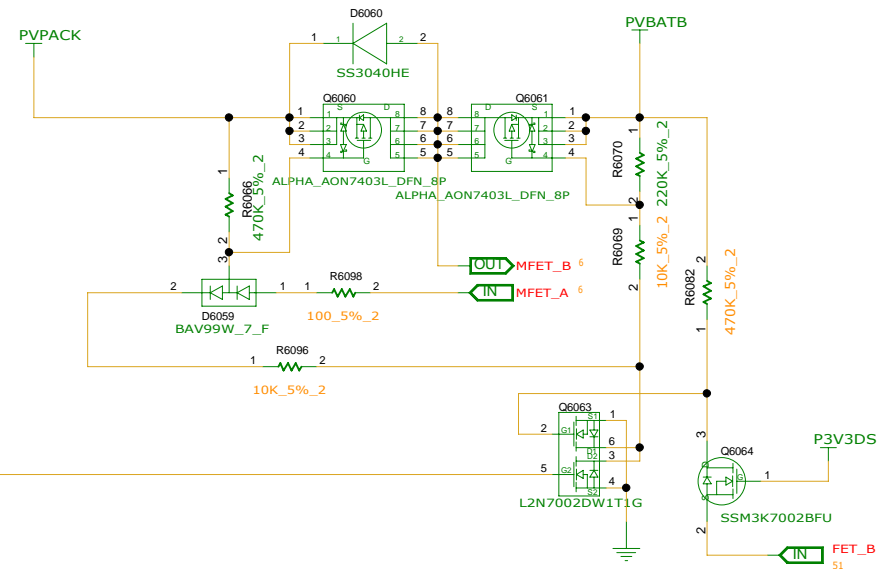
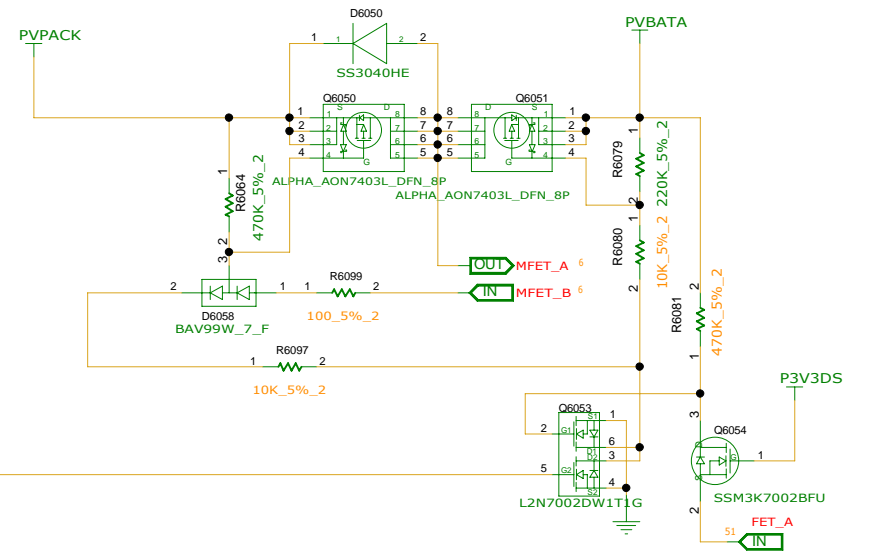
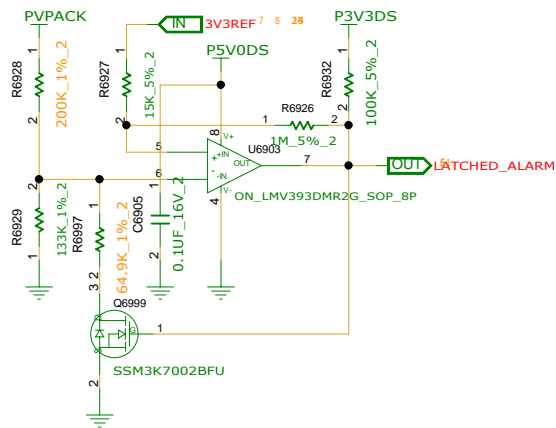
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INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
BLOCK DIAGRAM POWER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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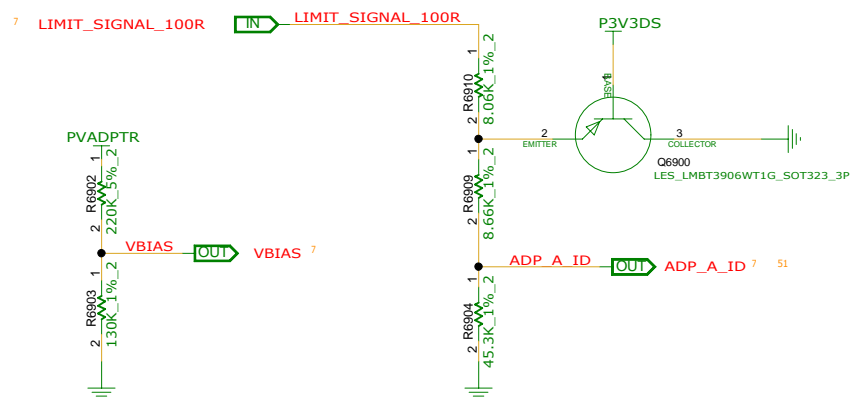
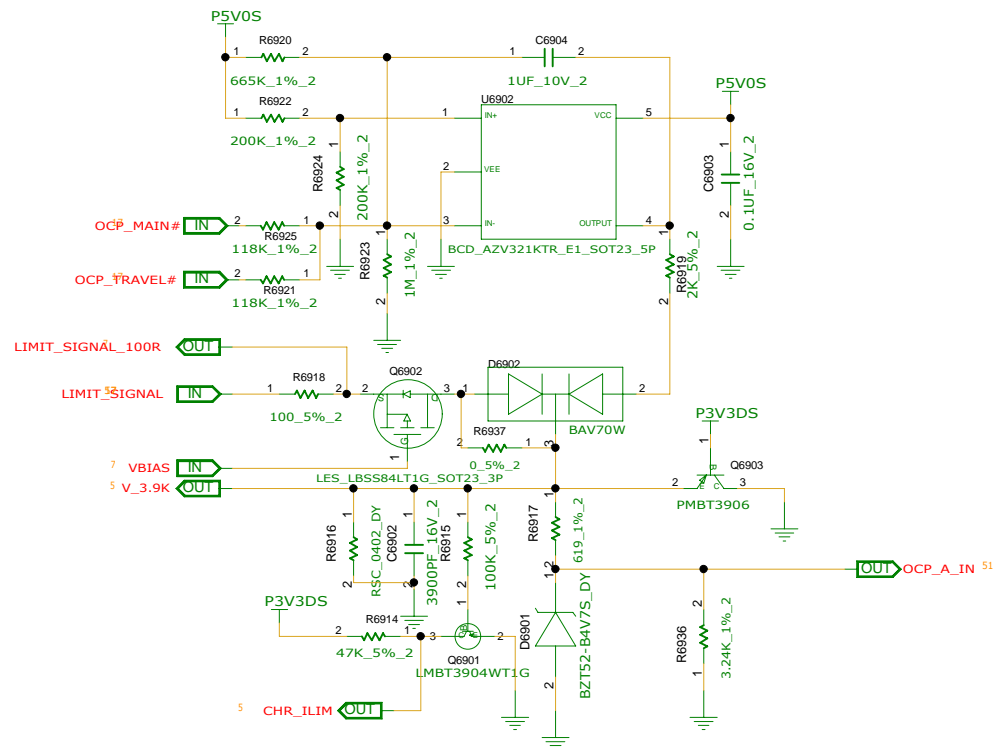
LATCHED_ALARM OUT

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram
DOC NUMBER
1310xxxxx-0-0
REV
X01

CHANGE by XXX DATE 21-OCT-2002

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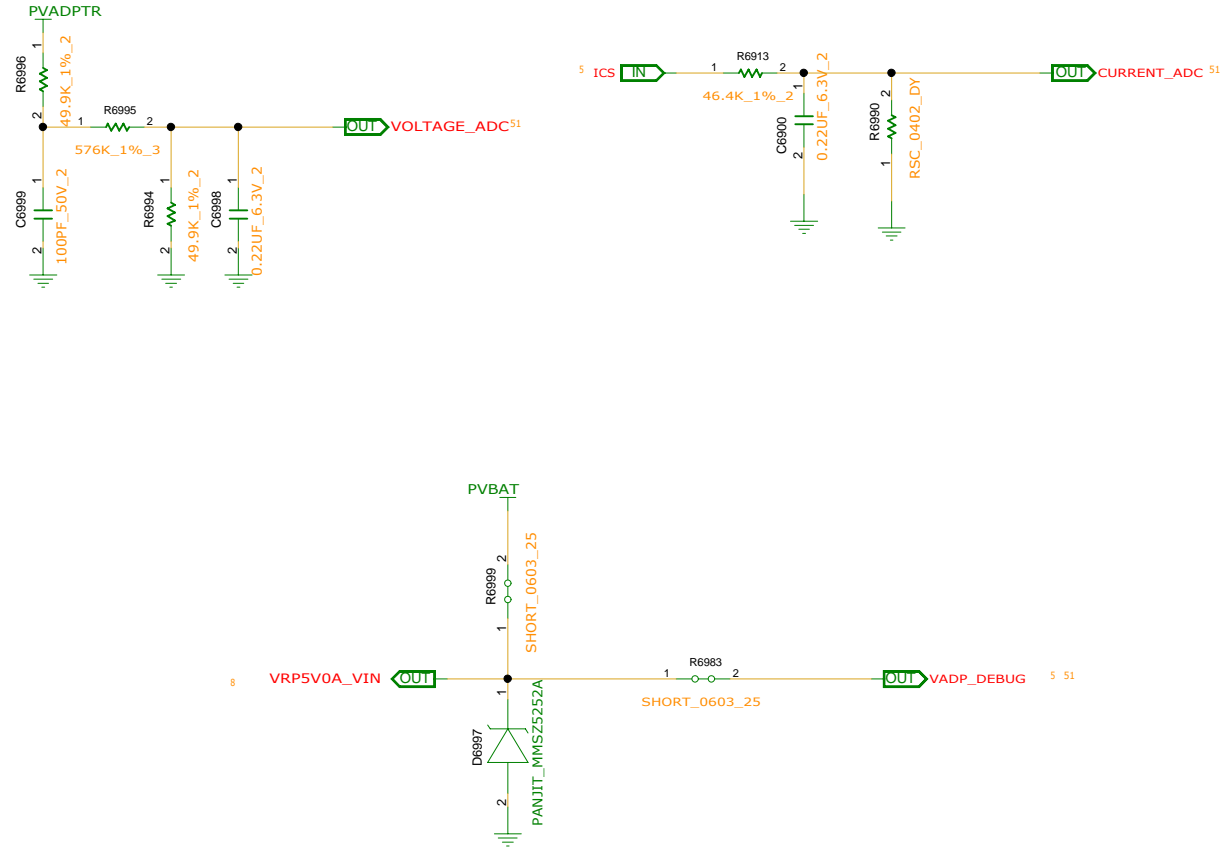


CHANGE by	XXX	DATE	21-OCT-2002
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TPS51285 IS NOT READY FOR DB0 USE TPS51225 FOR FIRST BUILD

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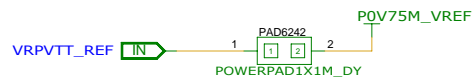
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

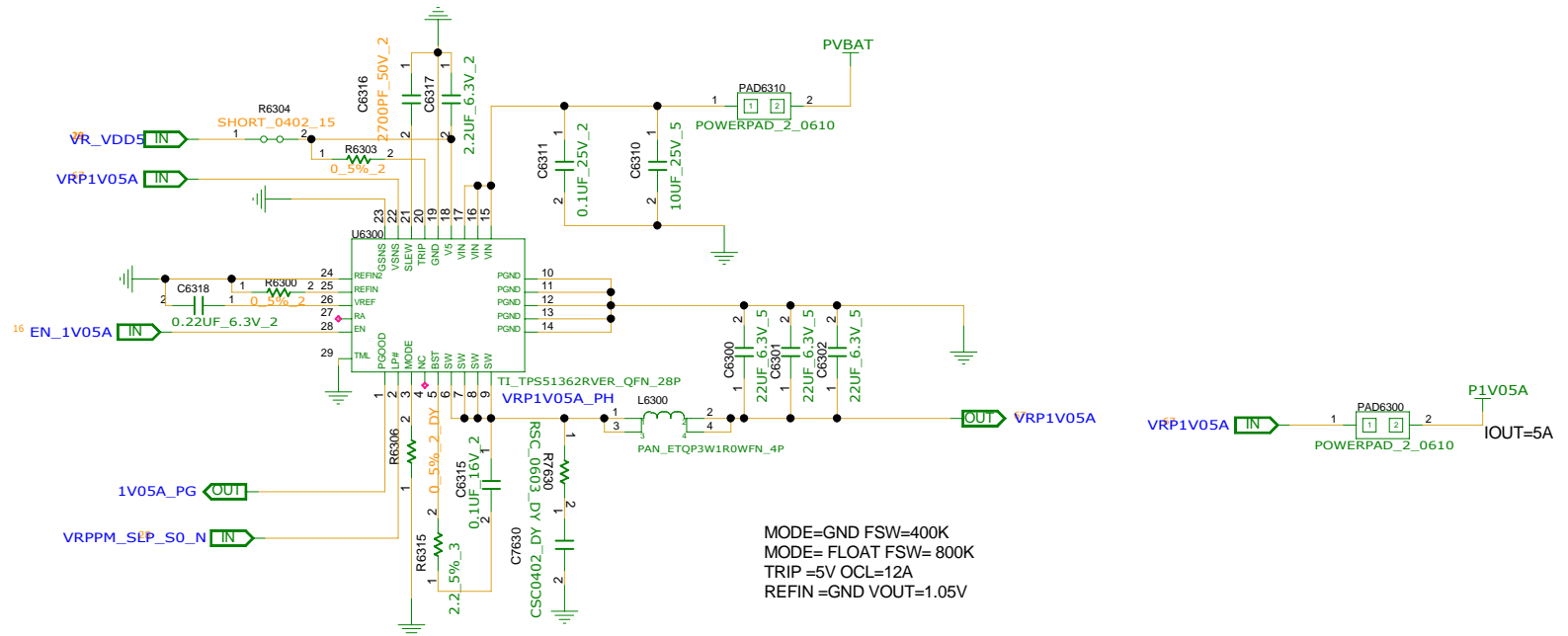
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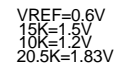


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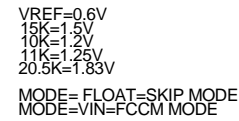
TITLE
 MODEL,PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

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CHANGE by	XXX	DATE	21-OCT-2002
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Block Diagram	
DOC NUMBER	SE

14 CPU_SKIP#

P5V0S

C6623
2.2UF_6.3V_2

VRPVCORE_PH

TI_CSD97374CQ4M_SON_8P

VRPVCORE_PH

CPU_PWM1

PVBAT_VCORE

PCME063T-R24MS1R195

R6601

R6600

R6603

C6605

0.1UF_25V_2

CPU_CSN1

CPU_CSP1

PVCORE

PVBAT

PAD6600
POWERPAD_2_0610

PVBAT_VCORE

C6601

10UF_25V_5

C6600

10UF_25V_5

OUT

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

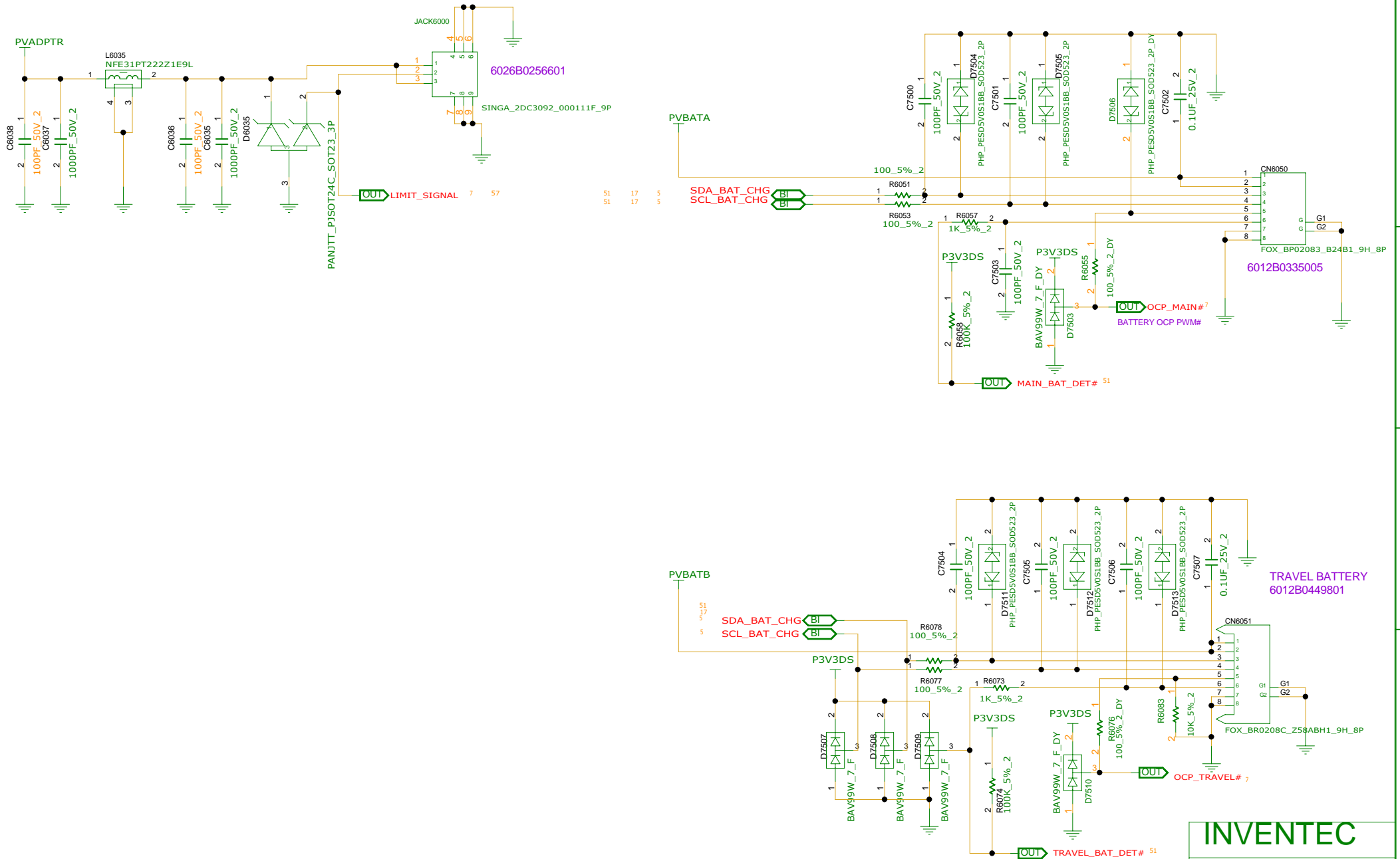
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1310xxxxx-0-0

REV
X01

CHANGE by XXX DATE 21-OCT-2002

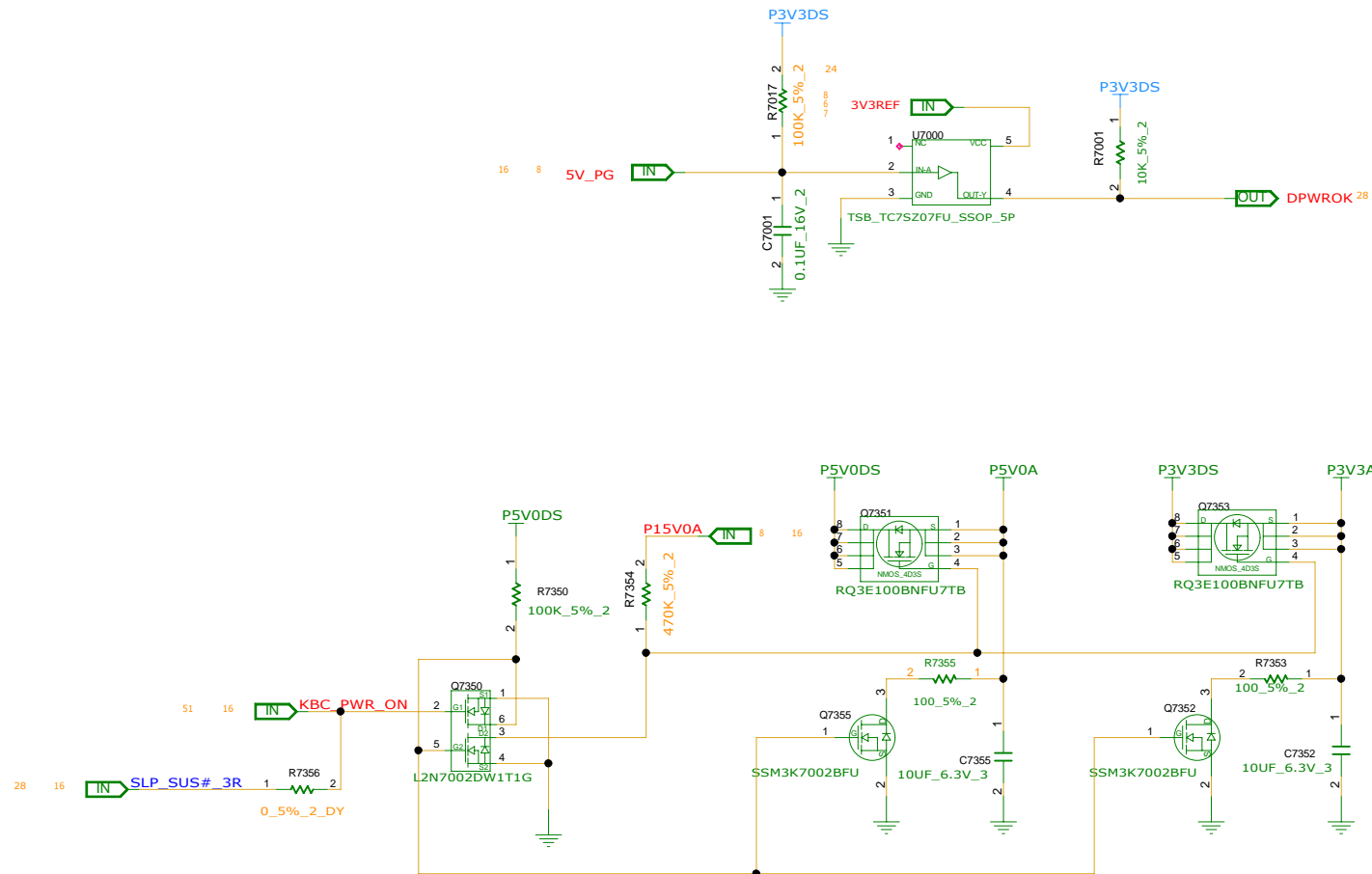
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[illegible]



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



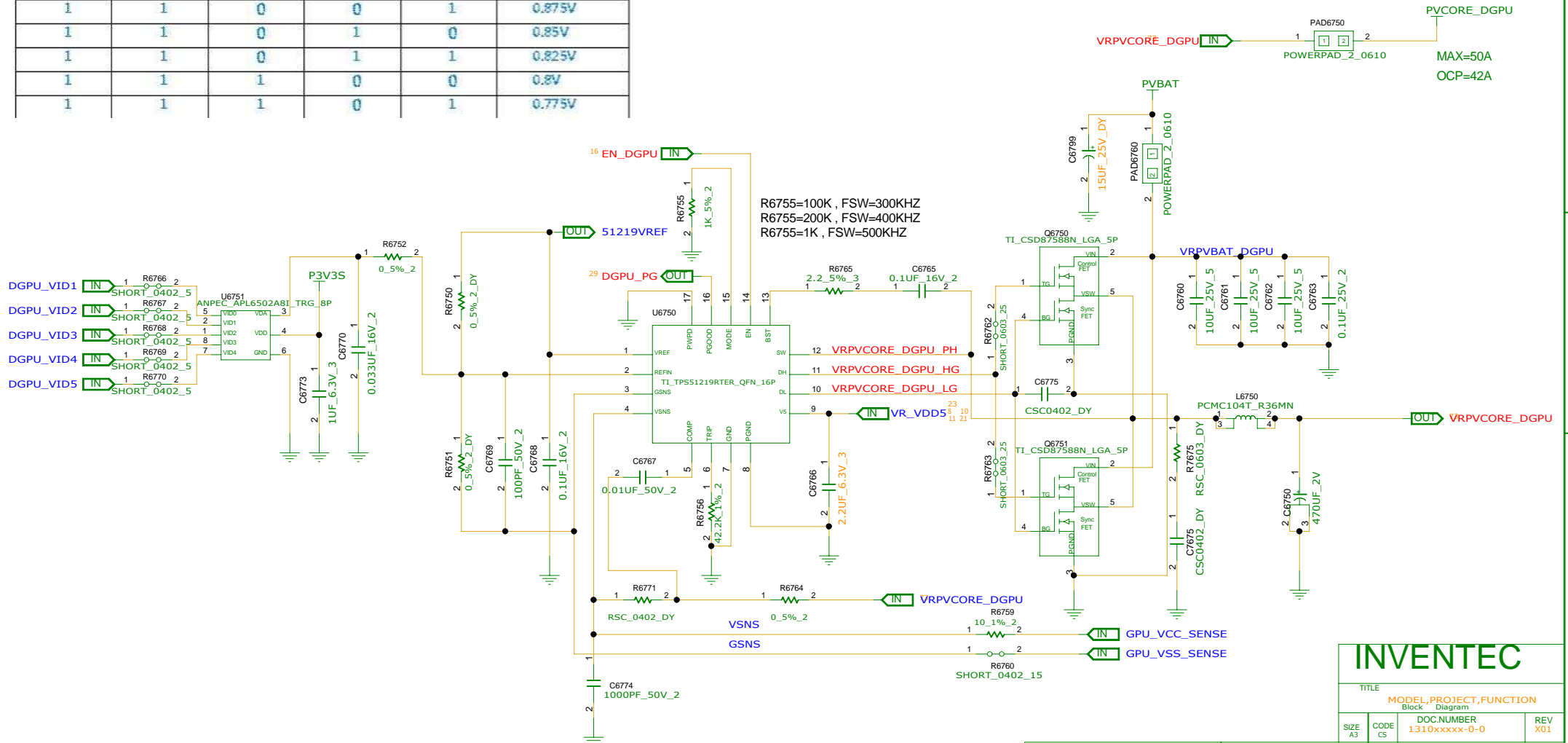
INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

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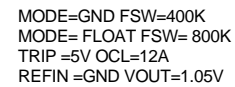
	C6762	C6752	L6750	Q6751	R6756
17W	OPEN	OPEN	ETQP4LR36AFM 6014B0164501	OPEN	80.6K_1% 6013A0072901
25W	install	install	PCMC104T-R36MN 6014B0024003	install	42.2K_1% 6013A0017701



EMPTY

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

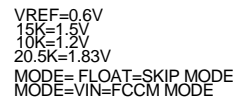


TITLE

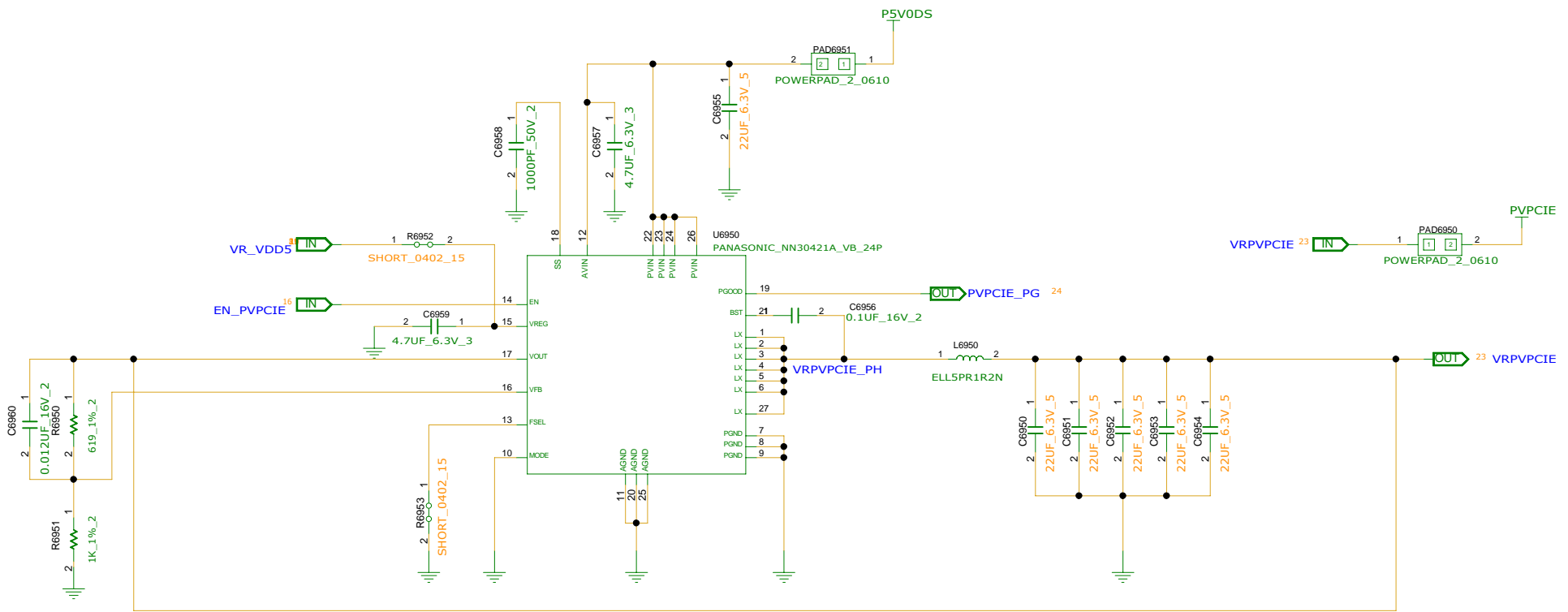
MODEL, PROJECT, FUNCTION

Block Diagram

CHANGE by	XXX	DATE	21-OCT-2002
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CHANGE by	XXX	DATE	21-OCT-2002
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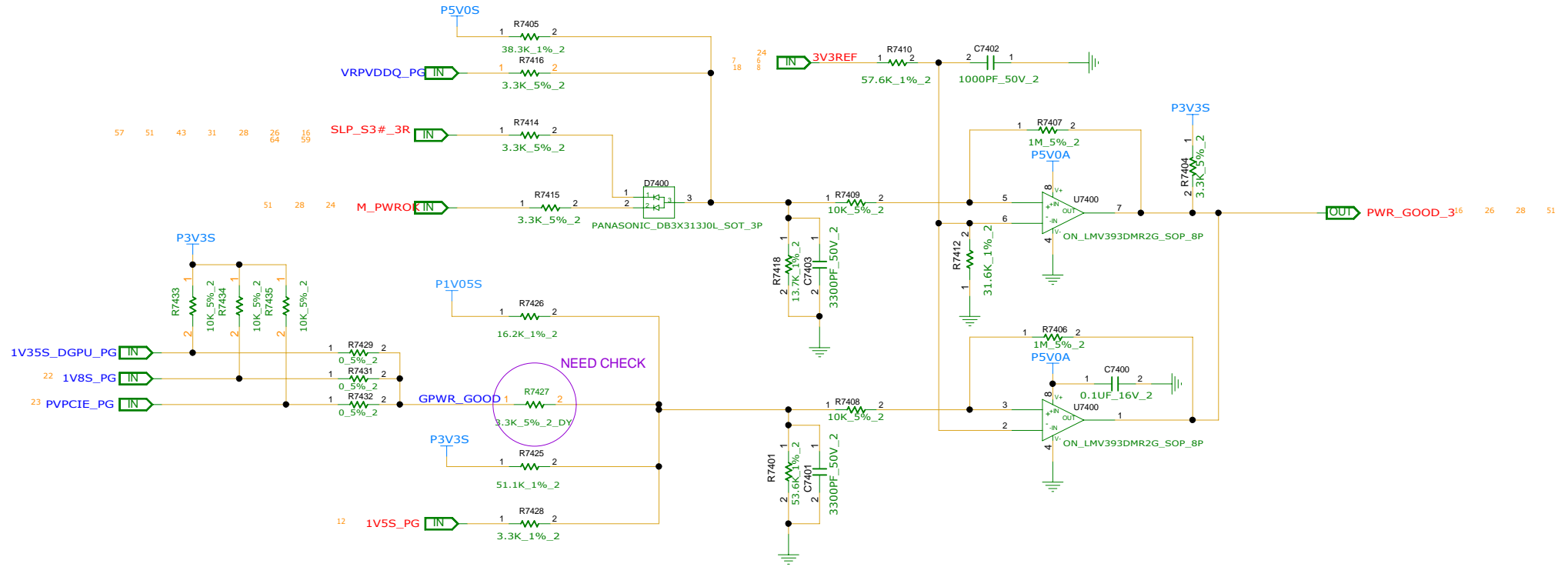
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

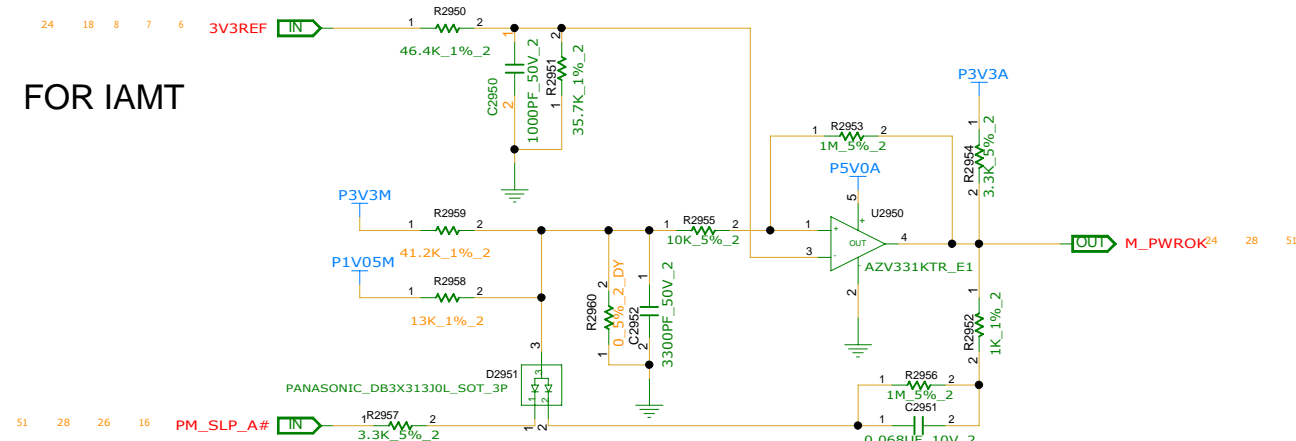
CHANGE by XXX DATE 21-OCT-2002

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REFERENCE NUMER : 7400~7450



FOR IAMT

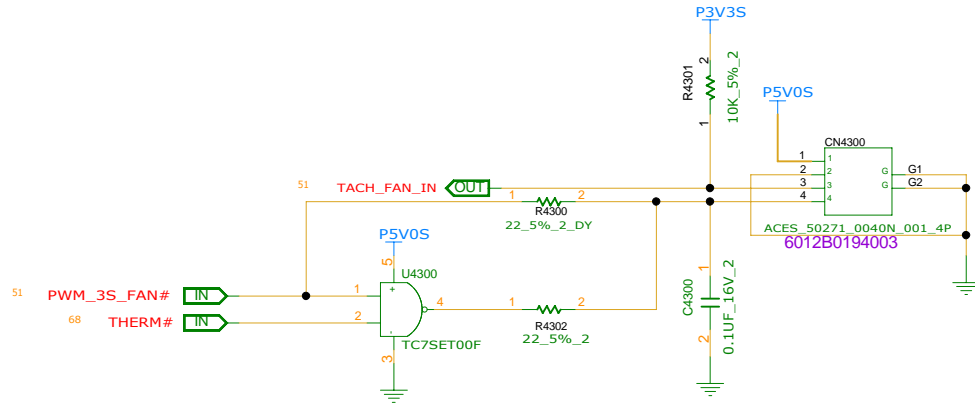


REFERENCE NUMER : 2950~2999

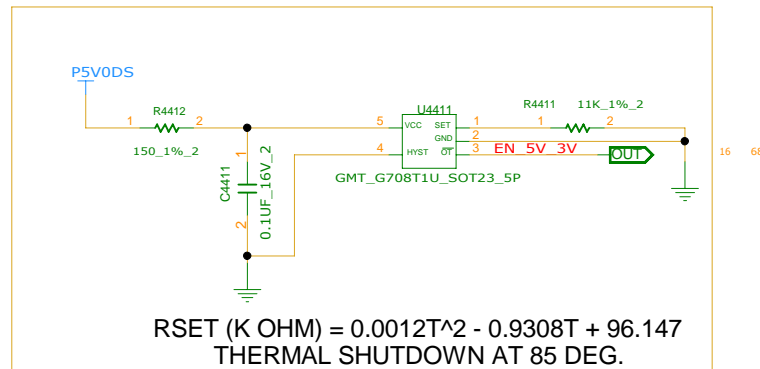
INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER (SEQUENCE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

REFERENCE NUMBER:4400~4349



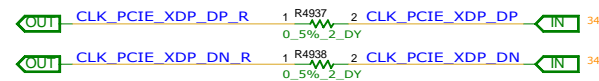
AMBIENT TEMP SENSE
WILL BE NOT USED IN 2013?



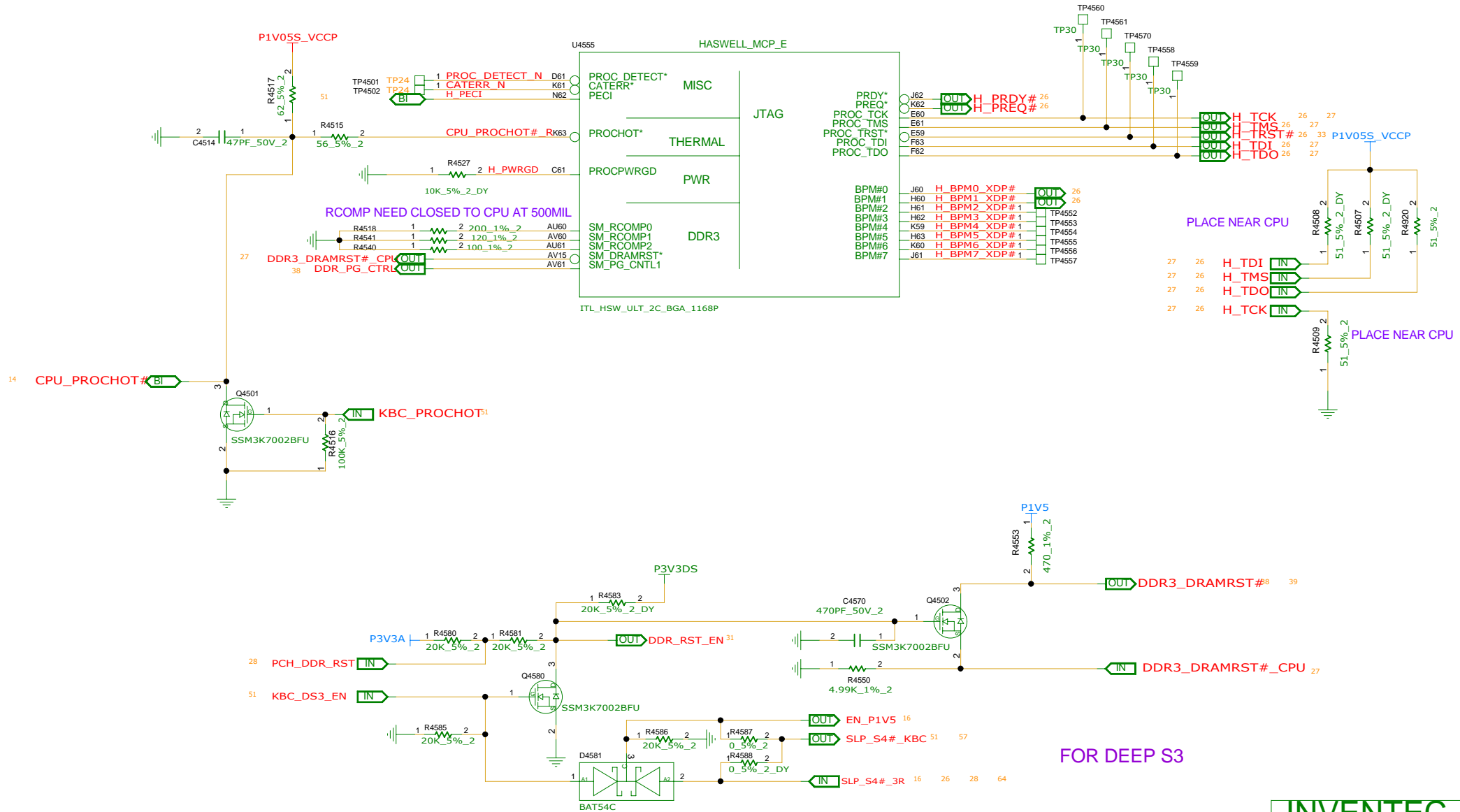
REFERENCE NUMBER:4411~4449

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION FAN & THERMAL			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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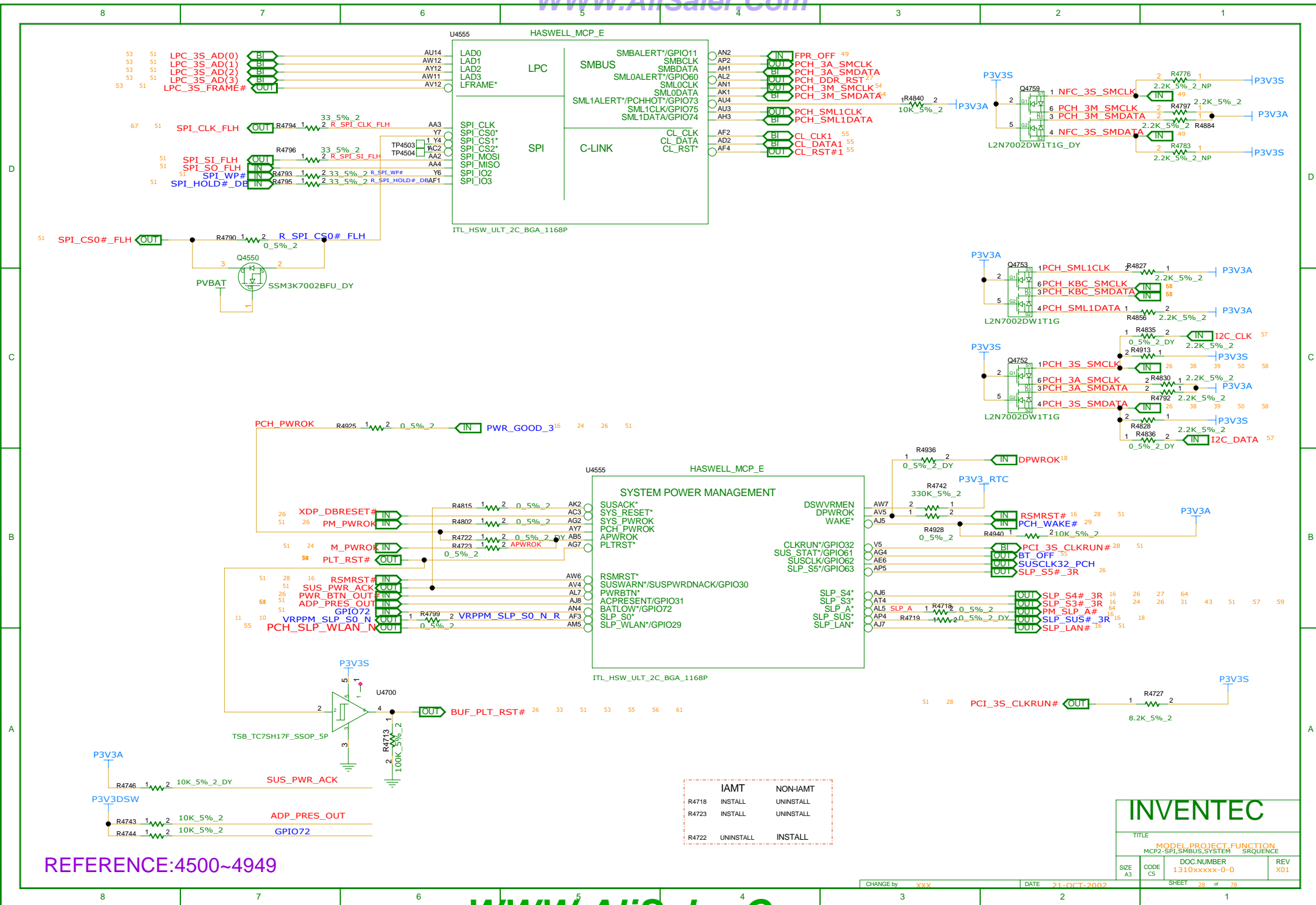


TITLE			
MODEL,PROJECT,FUNCTION			
XDP & ME CONN.			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	R X
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REFERENCE:4500~4949

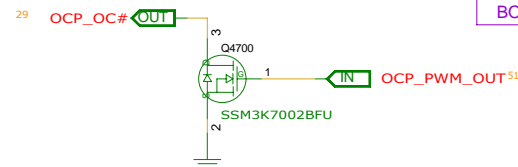
INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
MCP1-MISC			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by		DATE	SHEET
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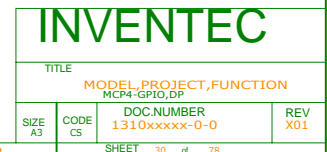
INVENTEC

TITLE		MODEL PROJECT	FUNCTION
		MCP2-SPI, SMBUS, SYSTEM	SEQUENCE
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

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REFERENCE:4500~4949



D

C

B

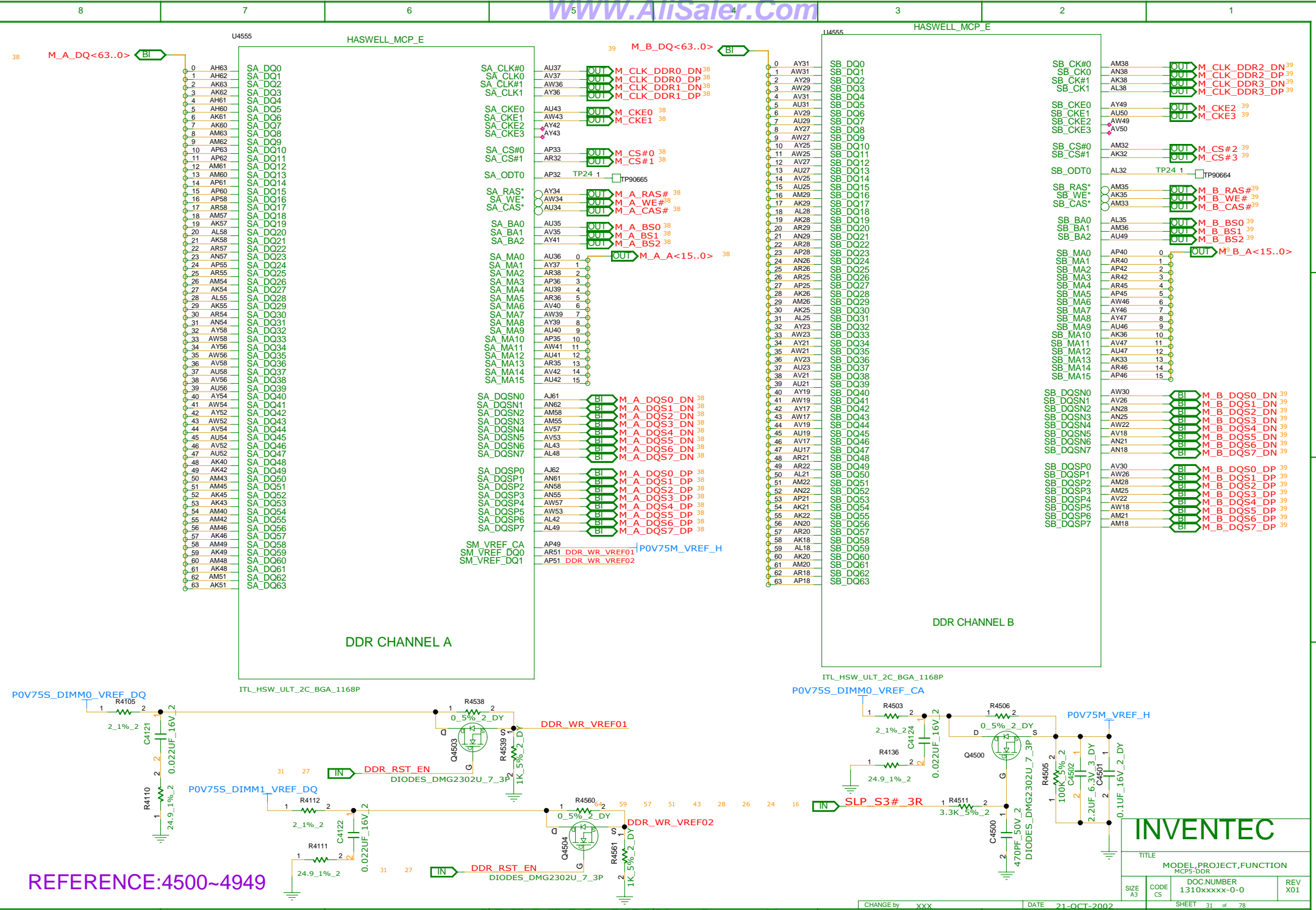
A

D

C

B

A





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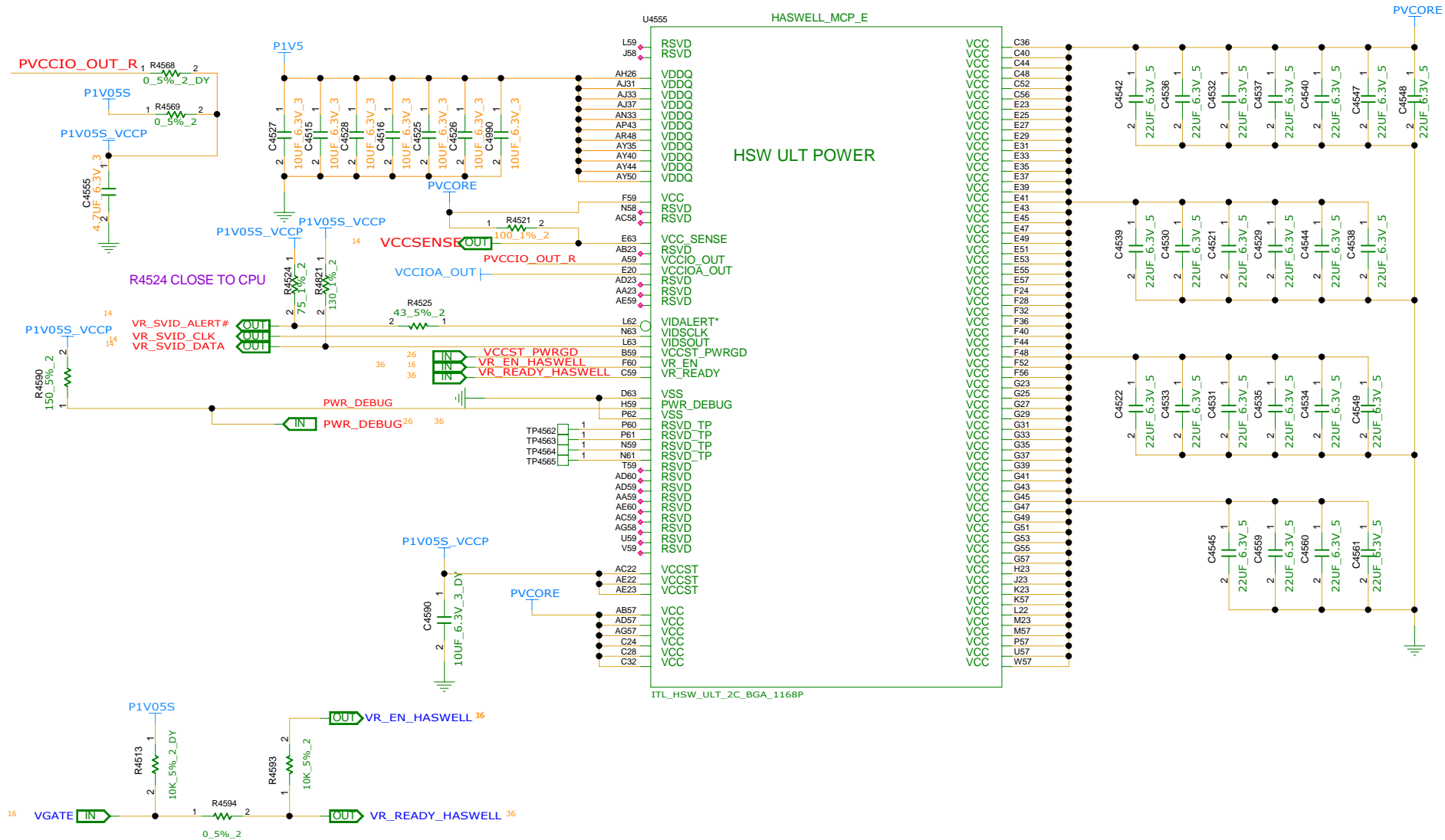
A

ITL_HSW_ULT_2C_BGA_1168P

CHANGE by	XXX	DATE	21-OCT-2002
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REFERENCE:4500~4949



INVENTEC

TITLE	MODEL,PROJECT,FUNCTION
1.1	1.1.1
1.2	1.2.1
1.3	1.3.1
1.4	1.4.1
1.5	1.5.1
1.6	1.6.1
1.7	1.7.1
1.8	1.8.1
1.9	1.9.1
1.10	1.10.1
1.11	1.11.1
1.12	1.12.1
1.13	1.13.1
1.14	1.14.1
1.15	1.15.1
1.16	1.16.1
1.17	1.17.1
1.18	1.18.1
1.19	1.19.1
1.20	1.20.1
1.21	1.21.1
1.22	1.22.1
1.23	1.23.1
1.24	1.24.1
1.25	1.25.1
1.26	1.26.1
1.27	1.27.1
1.28	1.28.1
1.29	1.29.1
1.30	1.30.1
1.31	1.31.1
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1.33	1.33.1
1.34	1.34.1
1.35	1.35.1
1.36	1.36.1
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1.39	1.39.1
1.40	1.40.1
1.41	1.41.1
1.42	1.42.1
1.43	1.43.1
1.44	1.44.1
1.45	1.45.1
1.46	1.46.1
1.47	1.47.1
1.48	1.48.1
1.49	1.49.1
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1.58	1.58.1
1.59	1.59.1
1.60	1.60.1
1.61	1.61.1
1.62	1.62.1
1.63	1.63.1
1.64	1.64.1
1.65	1.65.1
1.66	1.66.1
1.67	1.67.1
1.68	1.68.1
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1.83	1.83.1
1.84	1.84.1
1.85	1.85.1
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1.94	1.94.1
1.95	1.95.1
1.96	1.96.1
1.97	1.97.1
1.98	1.98.1
1.99	1.99.1
2.00	2.00.1
2.01	2.01.1
2.02	2.02.1
2.03	2.03.1
2.04	2.04.1
2.05	2.05.1
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2.09	2.09.1
2.10	2.10.1
2.11	2.11.1
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2.14	2.14.1
2.15	2.15.1
2.16	2.16.1
2.17	2.17.1
2.18	2.18.1
2.19	2.19.1
2.20	2.20.1
2.21	2.21.1
2.22	2.22.1
2.23	2.23.1
2.24	2.24.1
2.25	2.25.1
2.26	2.26.1
2.27	2.27.1
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2.29	2.29.1
2.30	2.30.1
2.31	2.31.1
2.32	2.32.1
2.33	2.33.1
2.34	2.34.1
2.35	2.35.1
2.36	2.36.1
2.37	2.37.1
2.38	2.38.1
2.39	2.39.1
2.40	2.40.1
2.41	

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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D

C

B

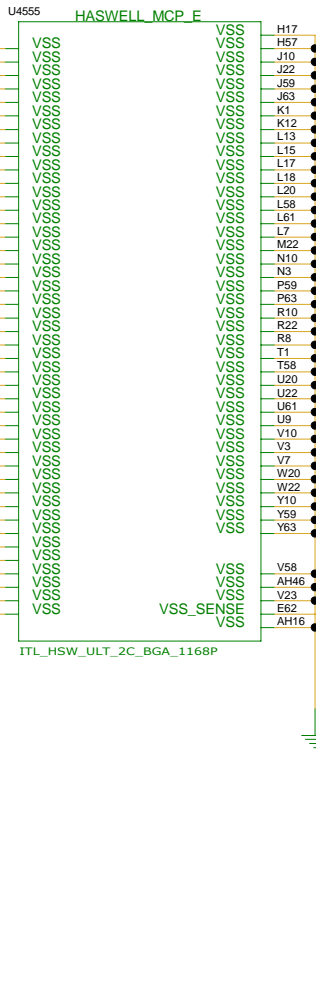
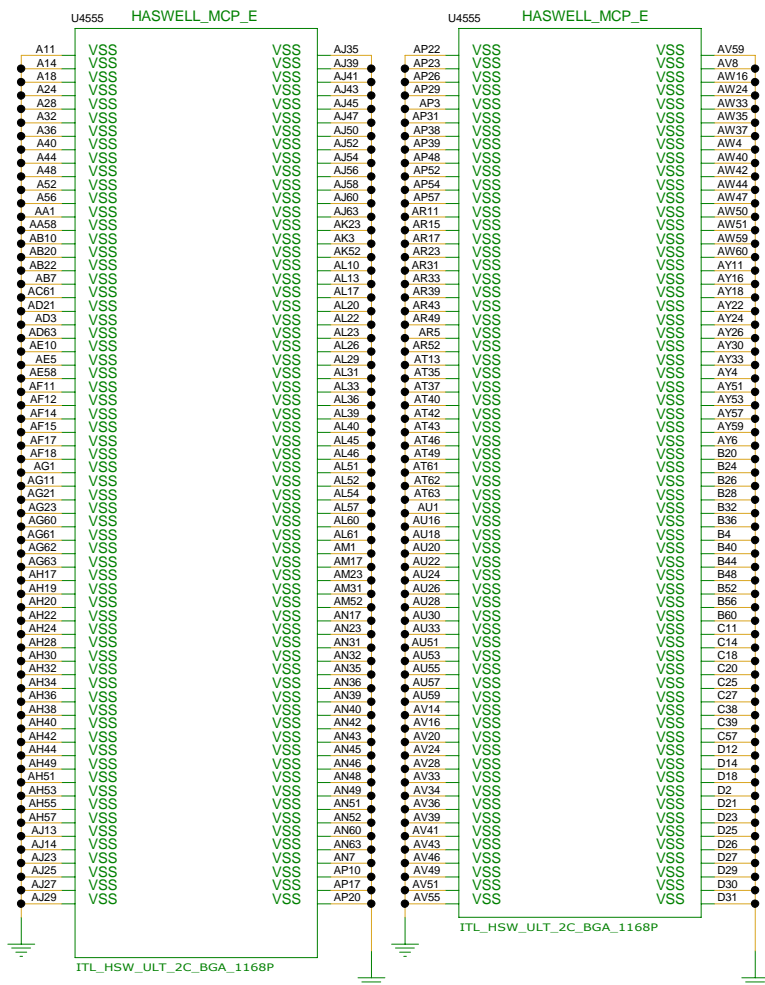
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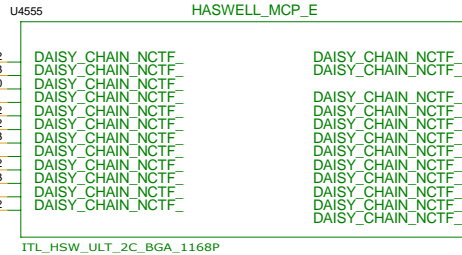
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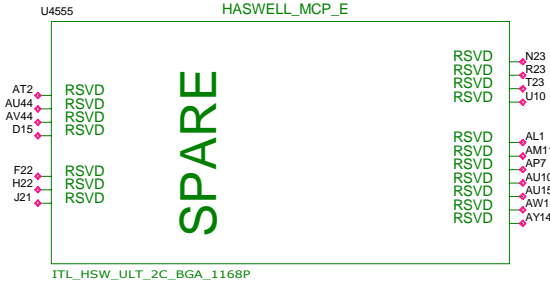
A



DC TEST AY2 AW2 AY2
DC TEST AY3 AW3 AY3
TP DC TEST AY60 AY60
DC TEST AY61 AW61 AY61
DC TEST AY62 AW62 AY62
DC TEST A3 B3 B2
DC TEST A3 B3 B3
DC TEST A61 B61 B61
DC TEST B62 B63 B62
DC TEST C1 C2 C1
DC TEST C1 C2 C2



A3 DC TEST A3 B3
A4 TP DC TEST A4
A60 TP DC TEST A60
A61 DC TEST A61 B61
A62 DC TEST B62 B63
AV1 TP DC TEST AV1
AW1 DC TEST AY2 AW2
AW2 DC TEST AY2 AW2
AW3 DC TEST AY3 AW3
AW61 DC TEST AY61 AW61
AW62 DC TEST AY62 AW62
AW63 DC TEST AY62 AW62



REFERENCE:4500~4949

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
MCP11-GND,DAISY_CHAIN,RESERVE			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by XXX			
DATE 21-OCT-2002			
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NOTE:

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32

REFERENCE NUMBER:4100~4299

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION DDR3_SO-DIMM0			
SIZE C	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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RESERVED

INVENTEC

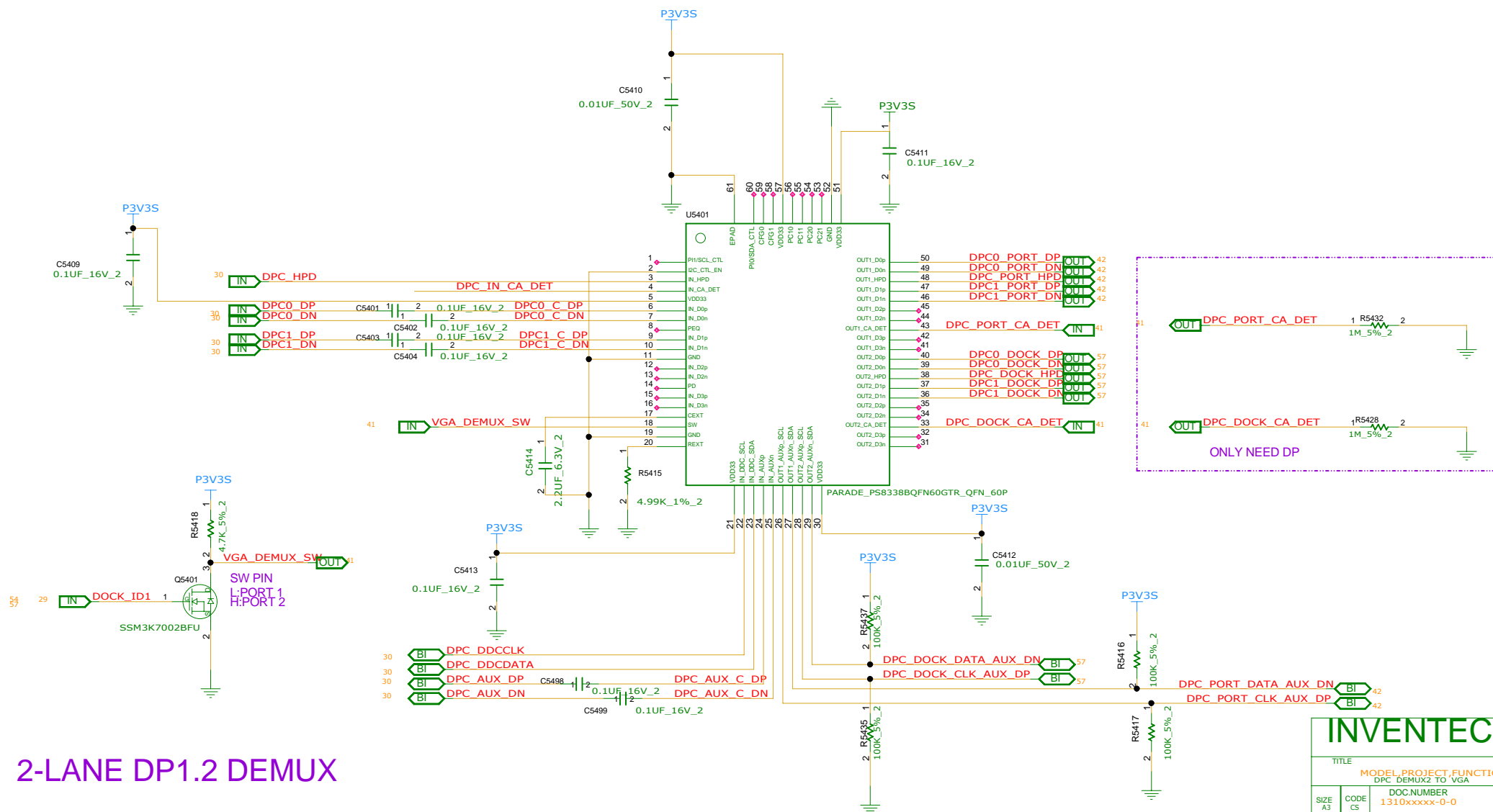
TITLE
MODEL PROJECT FUNCTION
DPB DEMUX1 TO DP

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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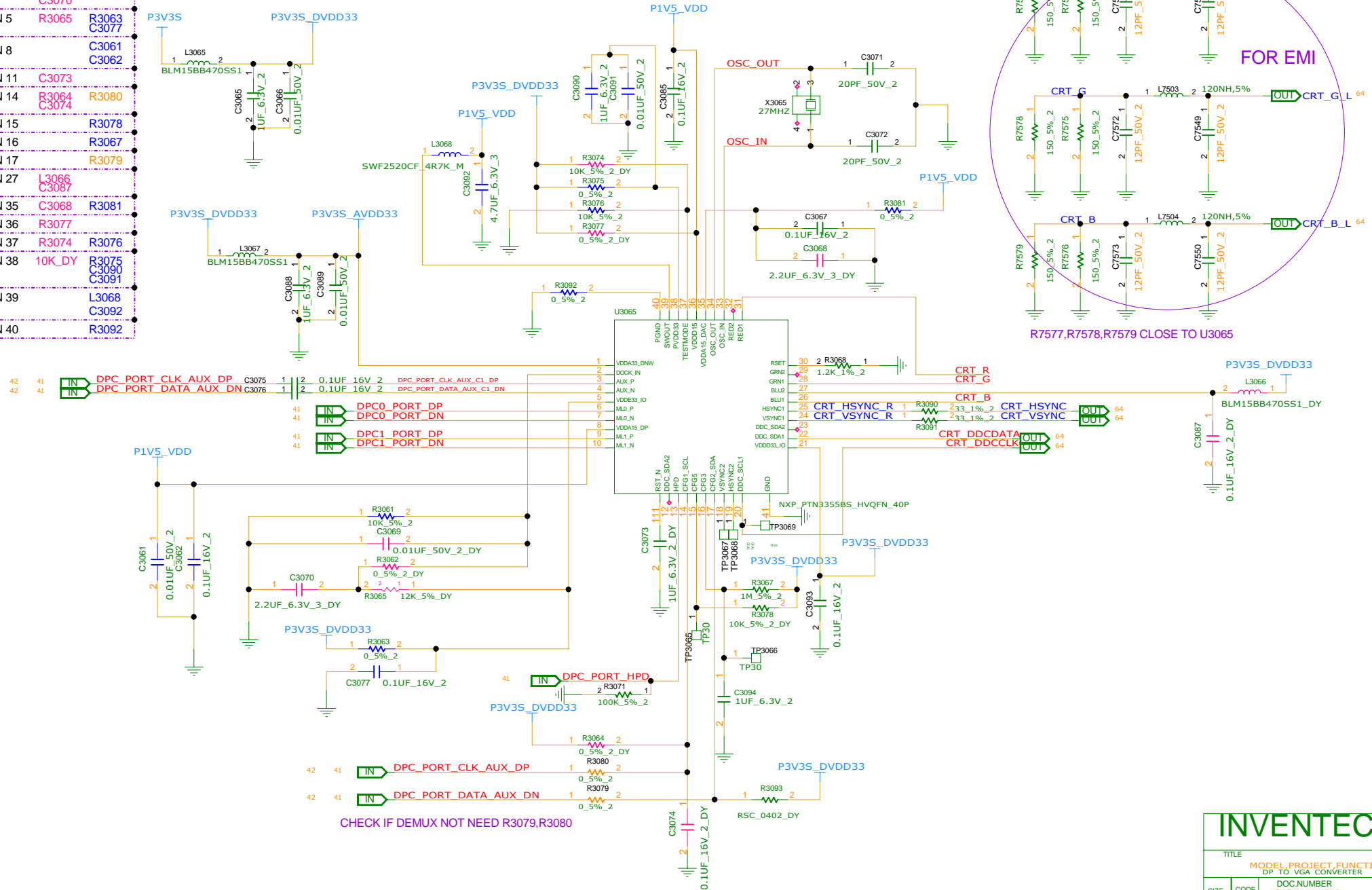
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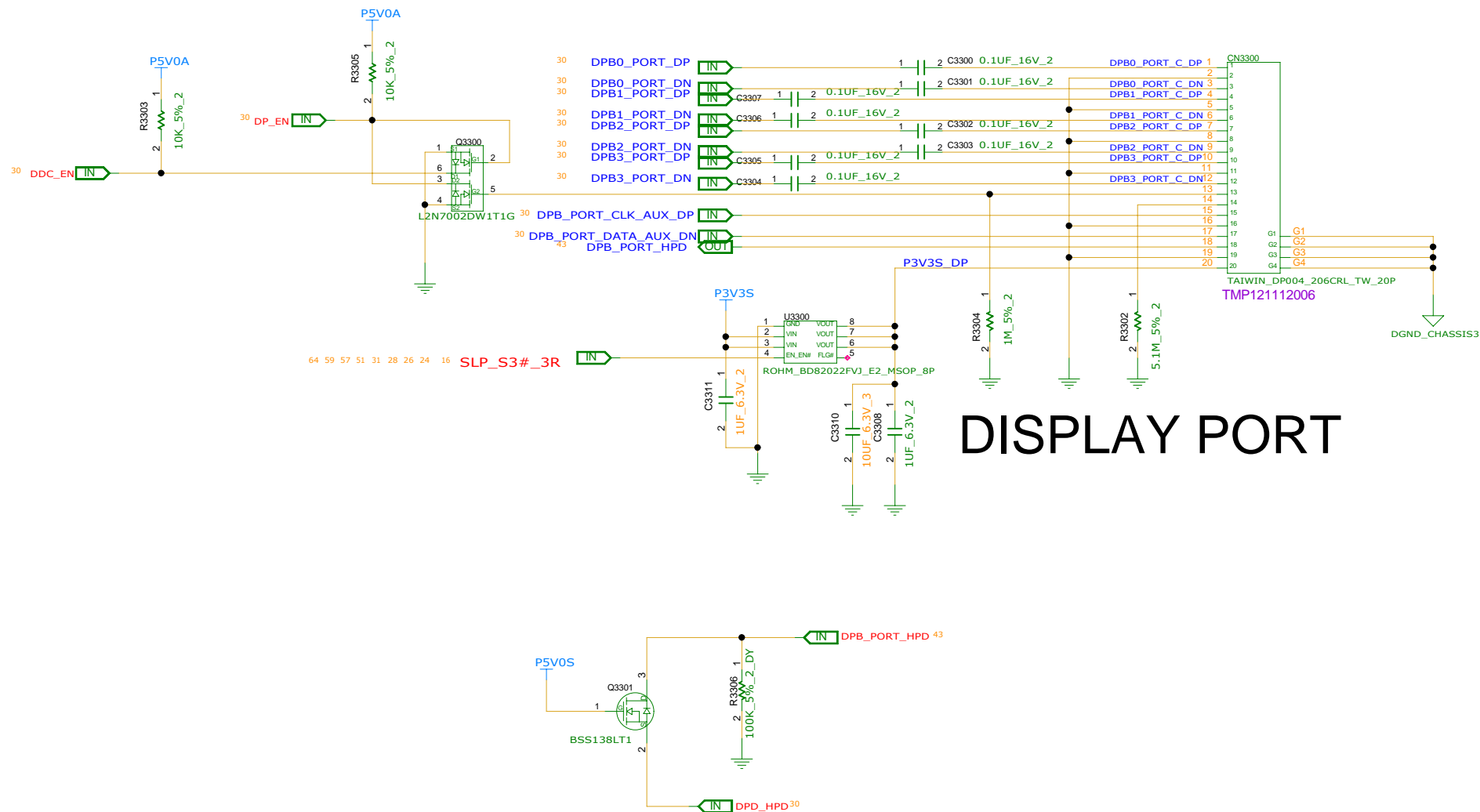


	3393	3355
PIN 2	R3062	R3061
	C3069	
	C3070	
PIN 5	R3065	R3063
	C3077	
PIN 8		C3061
		C3062
PIN 11	C3073	
PIN 14	R3064	R3080
	C3074	
PIN 15		R3078
PIN 16		R3067
PIN 17		R3079
PIN 27	L3066	
	C3087	
PIN 35	C3068	R3081
PIN 36	R3077	
PIN 37	R3074	R3076
PIN 38	10K_DY	R3075
		C3090
		C3091
PIN 39		L3068
		C3092
PIN 40		R3092



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
DP TO VGA CONVERTER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 42 of 78			



DISPLAY PORT

REFERENCE NUMBER:3300~3399

INVENTEC

TITLE MODEL PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 43 of 78

D

C

B

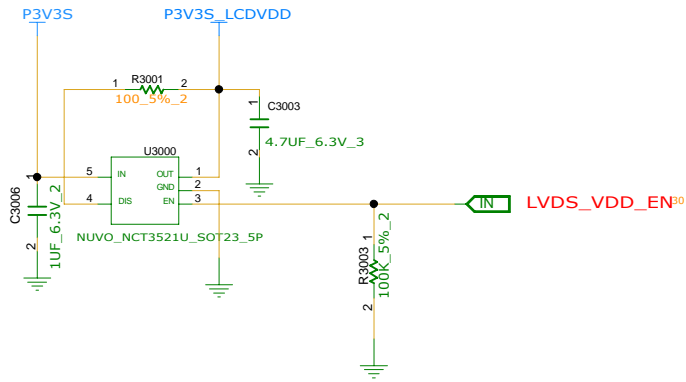
A

D

C

B

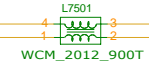
A



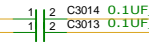
CHECK IF TOUCH PANEL NEED OTHER POWER RAIL

TOUCH PANEL

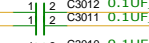
46 USB_D_P1_DN
46 USB_D_P1_DP



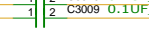
30 EDP_TX1_DN
30 EDP_TX1_DP



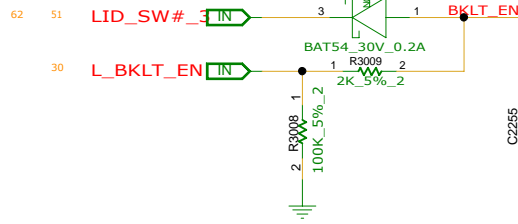
30 EDP_TX0_DN
30 EDP_TX0_DP



30 EDP_AUX_DP
30 EDP_AUX_DN



P3V3S_LCDVDD



BKLT_EN

P3V3S

P5V0S

INV_PWM

DMIC_CLK

DMIC_DATA

TOUCH_RST#

CAMERA_ON

USB_P6_DP

USB_P6_DN

USB_P6_DP_L

USB_P6_DN_L

EDP_HPDP#

PVBAT

ACES_50203_0400T_001_40P

6012B0431203

25-34 WEBCAM
35-36 LOGO LIGHT

INVENTEC

REFERENCE NUMBER:3000~3049

TITLE			
MODEL PROJECT,FUNCTION			
LCM & WEBCAM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 44 of 78

REFERENCE NUMBER:1700~1749

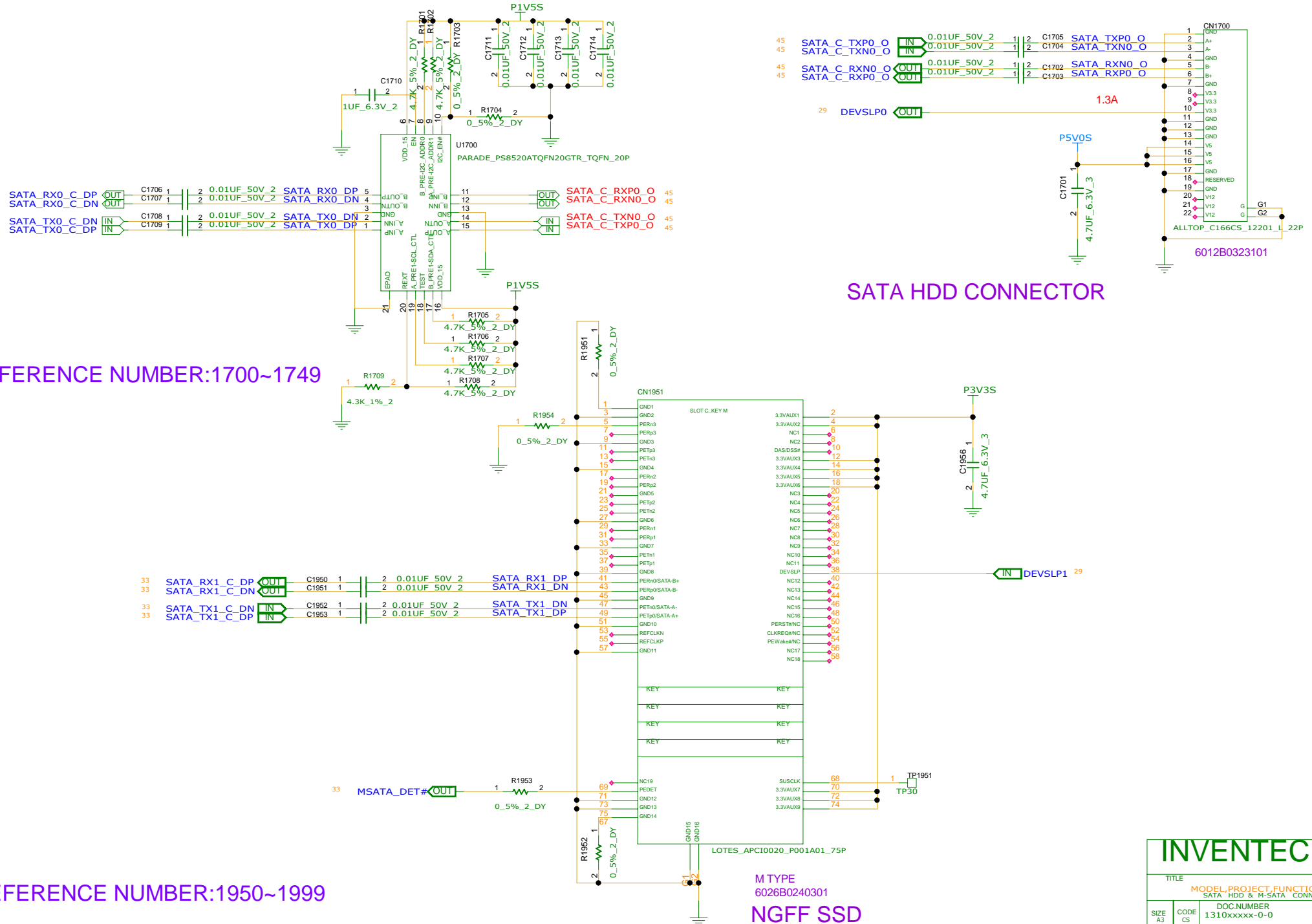
REFERENCE NUMBER:1950~1999

M TYPE
6026B0240301
NGFF SSD

SATA HDD CONNECTOR

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
SATA, HDD & M-SATA CONN.			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 45 of 78			



D

D

C

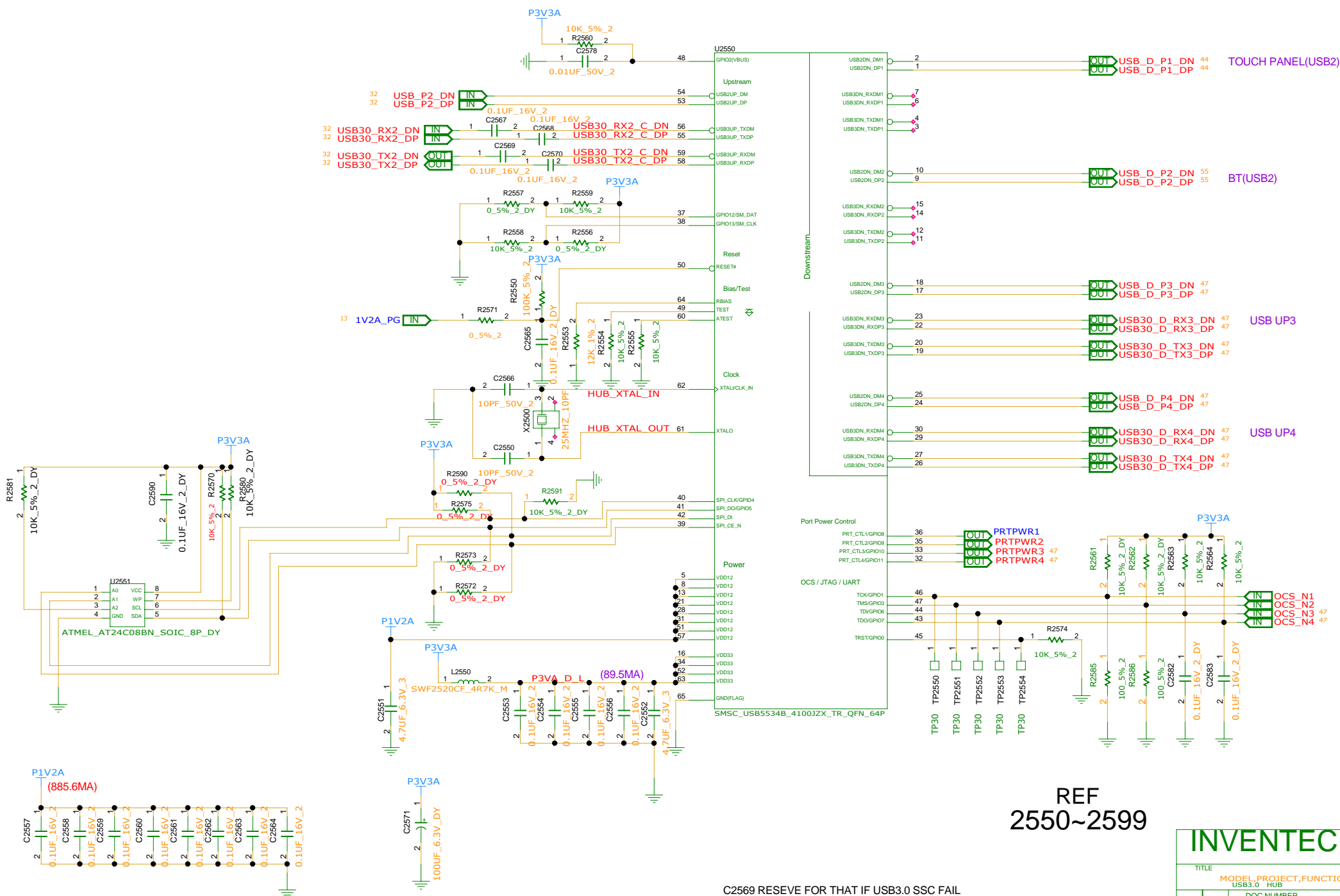
C

B

B

A

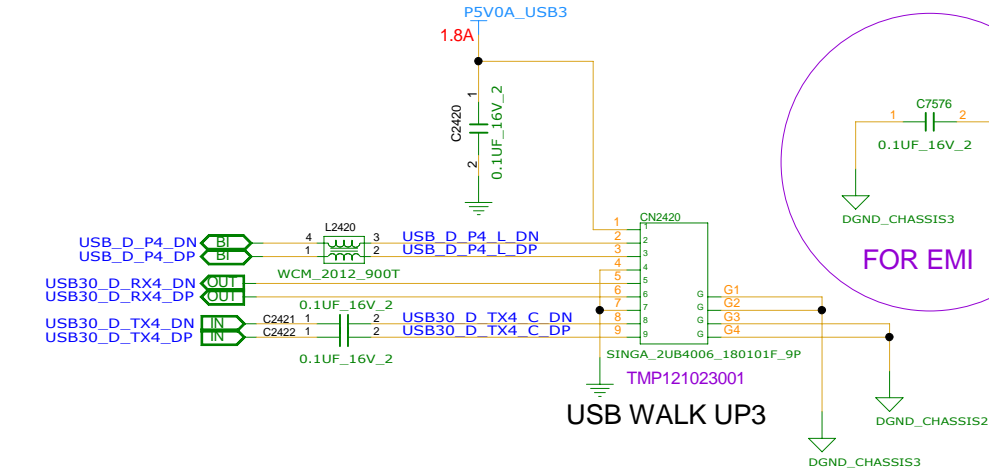
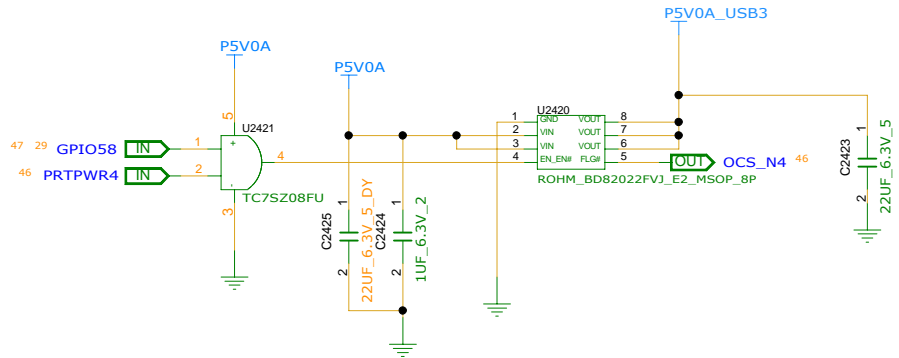
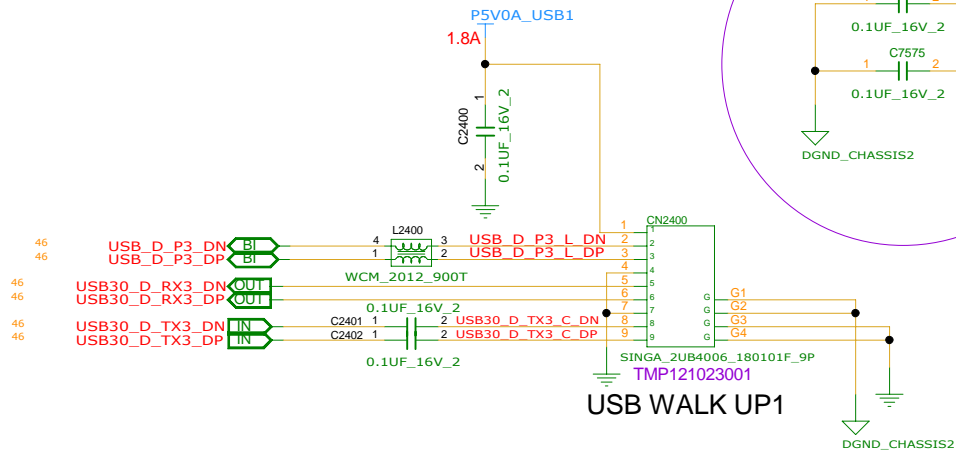
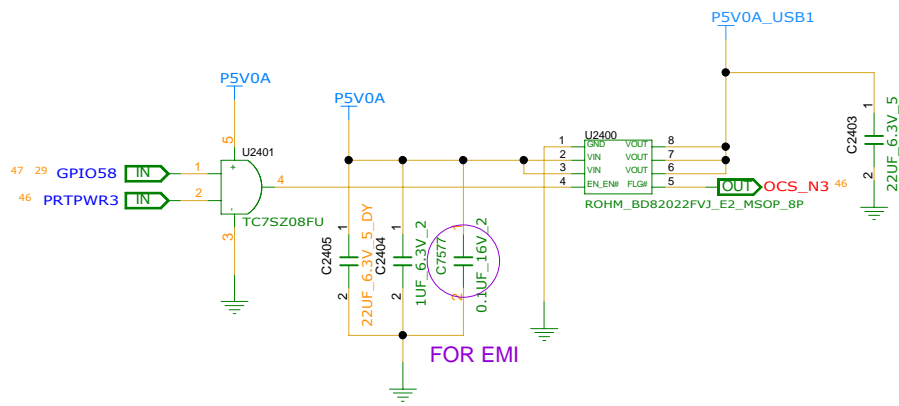
A



REF
2550~2599

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
USB3.0 HUB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

C2569 RESEVE FOR THAT IF USB3.0 SSC FAIL

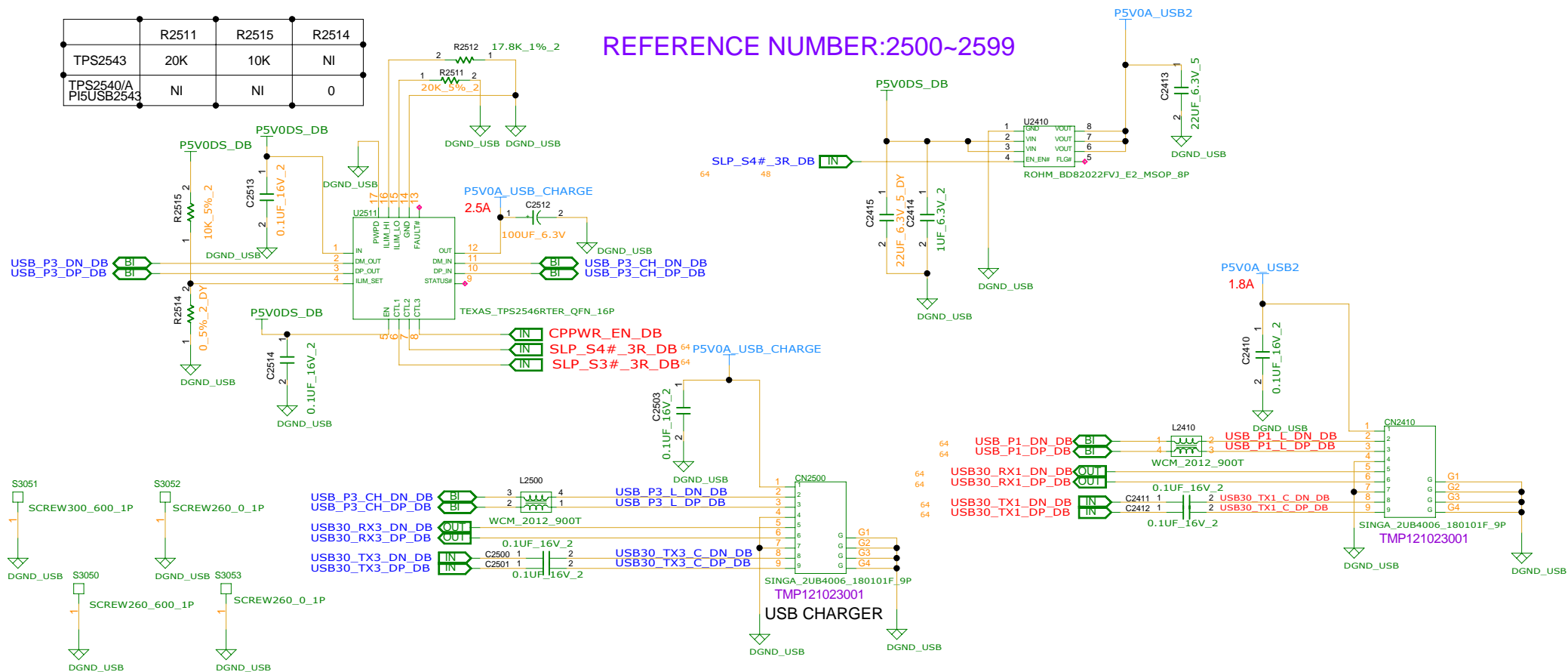


REFERENCE NUMBER:2400~2499

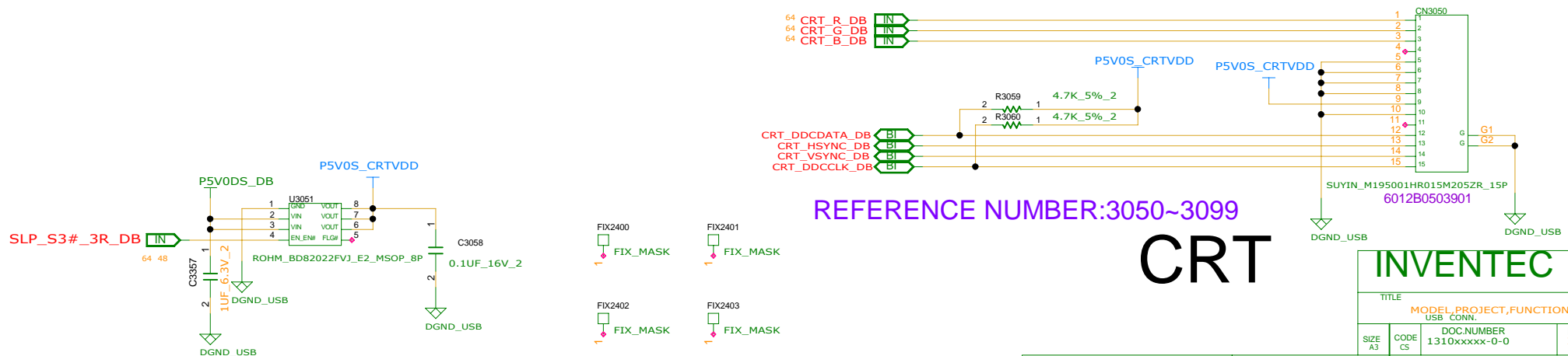
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
USB & USB CHARGER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE NUMBER:2500~2599



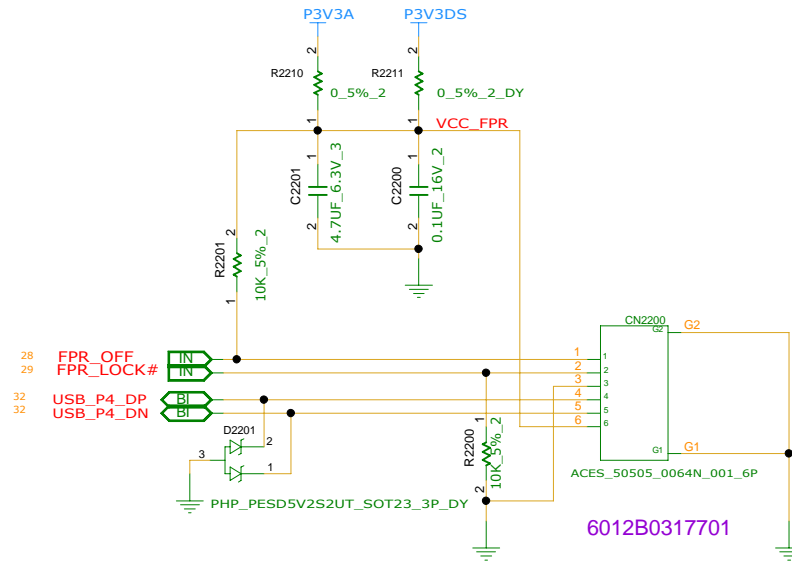
REFERENCE NUMBER:3050~3099



CRT

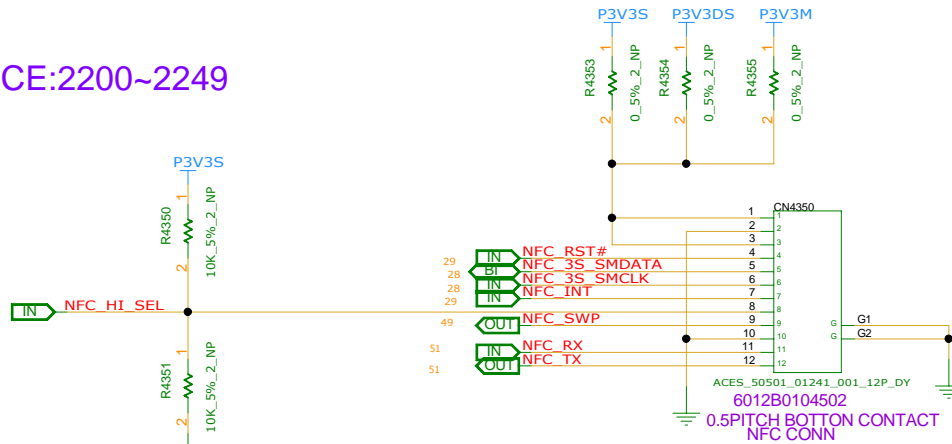
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 48 of 78			



FINGER PRINT CONN

REFERENCE:2200~2249



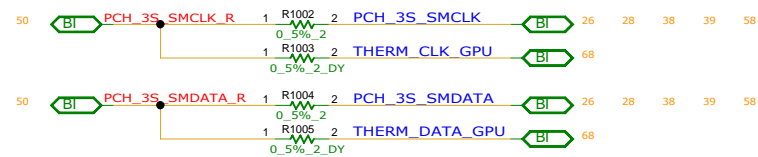
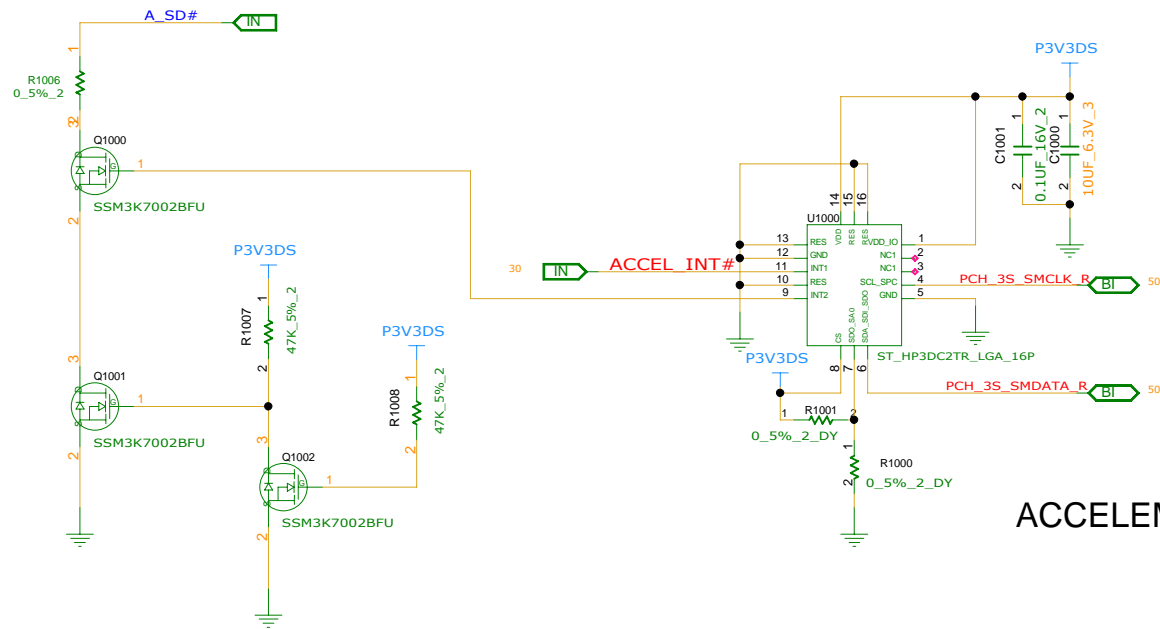
0.5PITCH BOTTOM CONTACT
NFC CONN

NFC_HI_SEL R4350 R4351
HIGH (UART) INSTALL
LOW (I2C) INSTALL

REFERENCE:4350~4399

INVENTEC

TITLE			
MODEL PROJECT FUNCTION FINGER PRINTER & NFC			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 49 of 78			



REFERENCE NUMER : 1000~1099

INVENTEC

TITLE MODEL PROJECT,FUNCTION

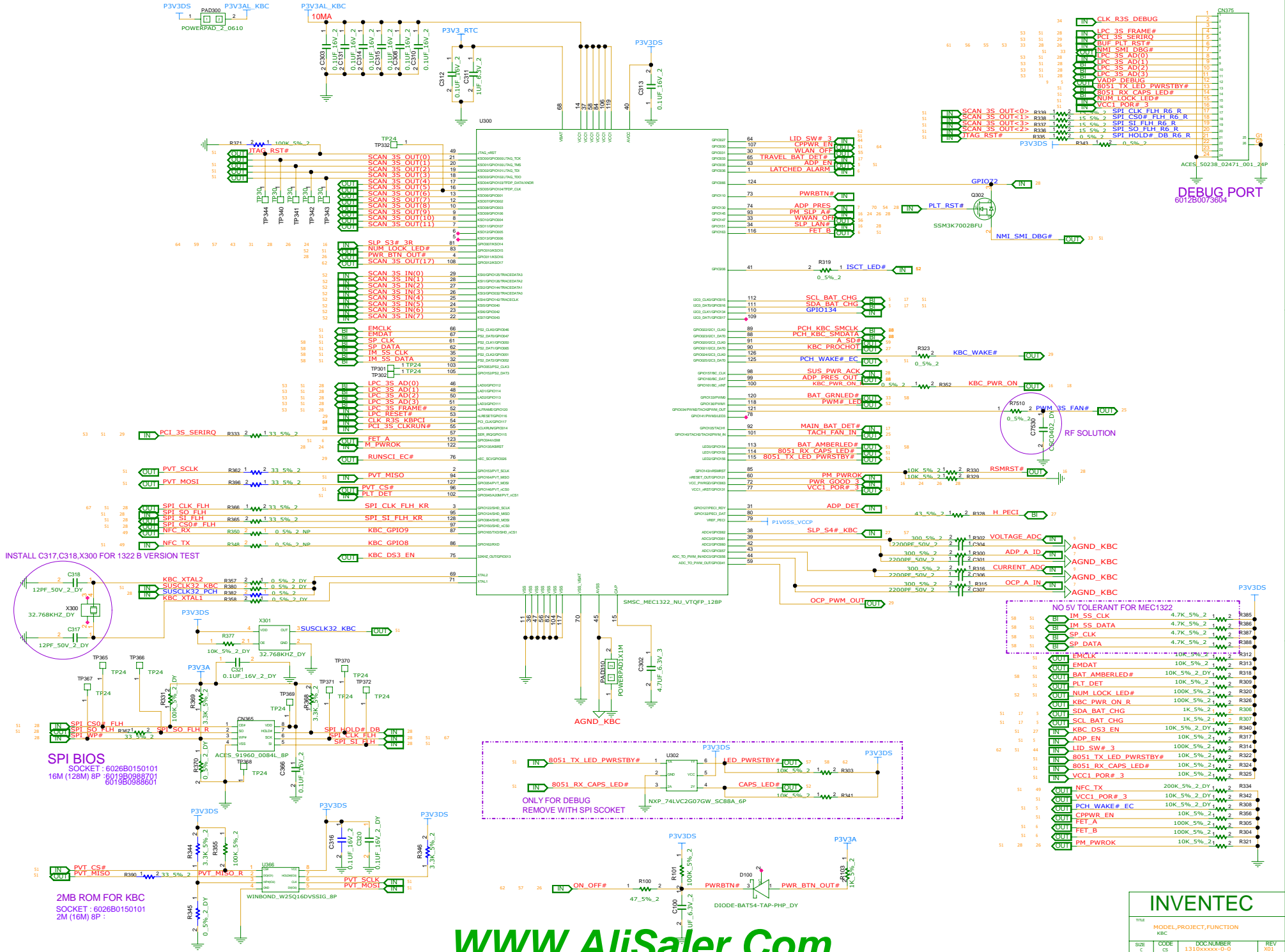
ACCELEMOTOR

DOC NUMBER 1310xxxxx-0-0

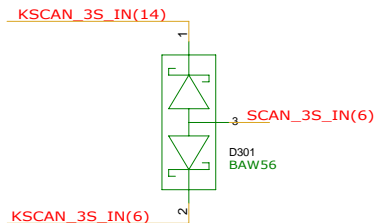
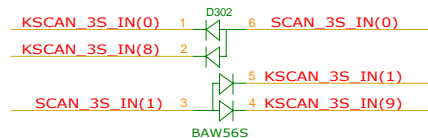
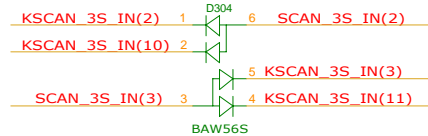
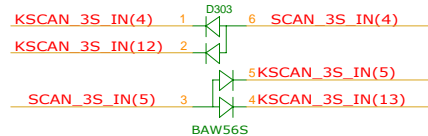
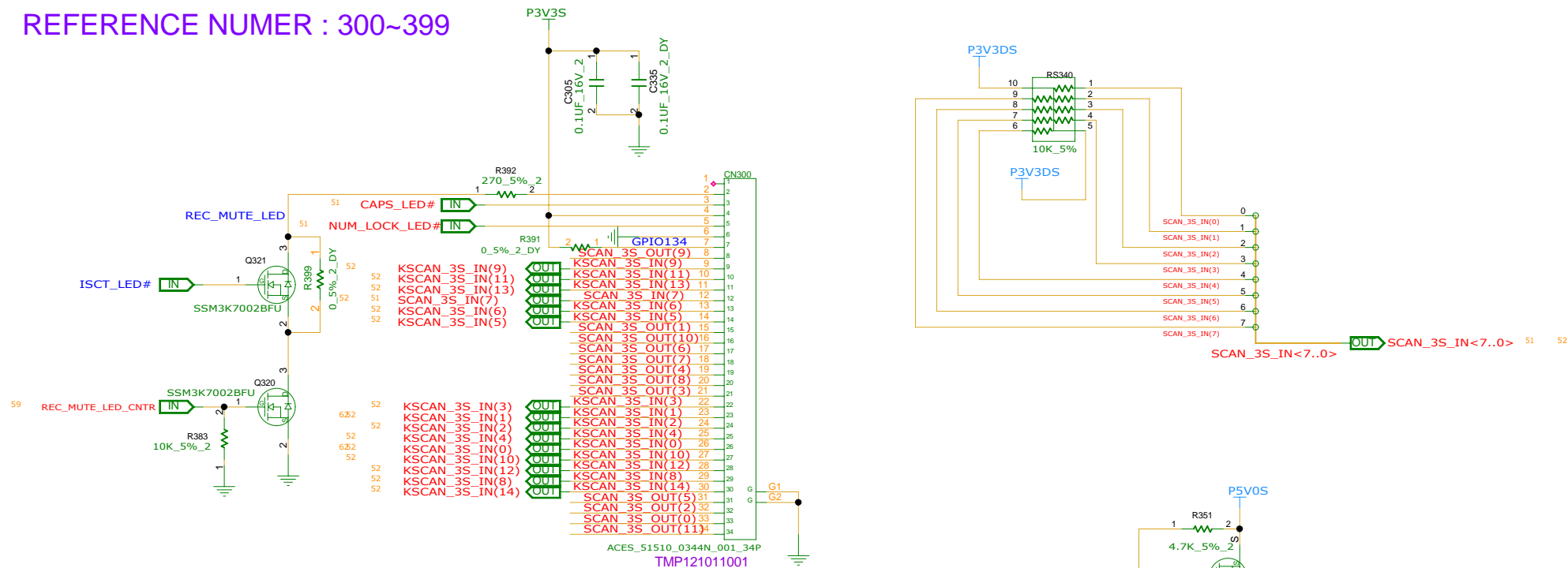
REV X01

SHEET 50 of 78

CHANGE by XXX DATE 21-OCT-2002



REFERENCE NUMBER : 300~399



INVENTEC

TITLE MODEL PROJECT,FUNCTION

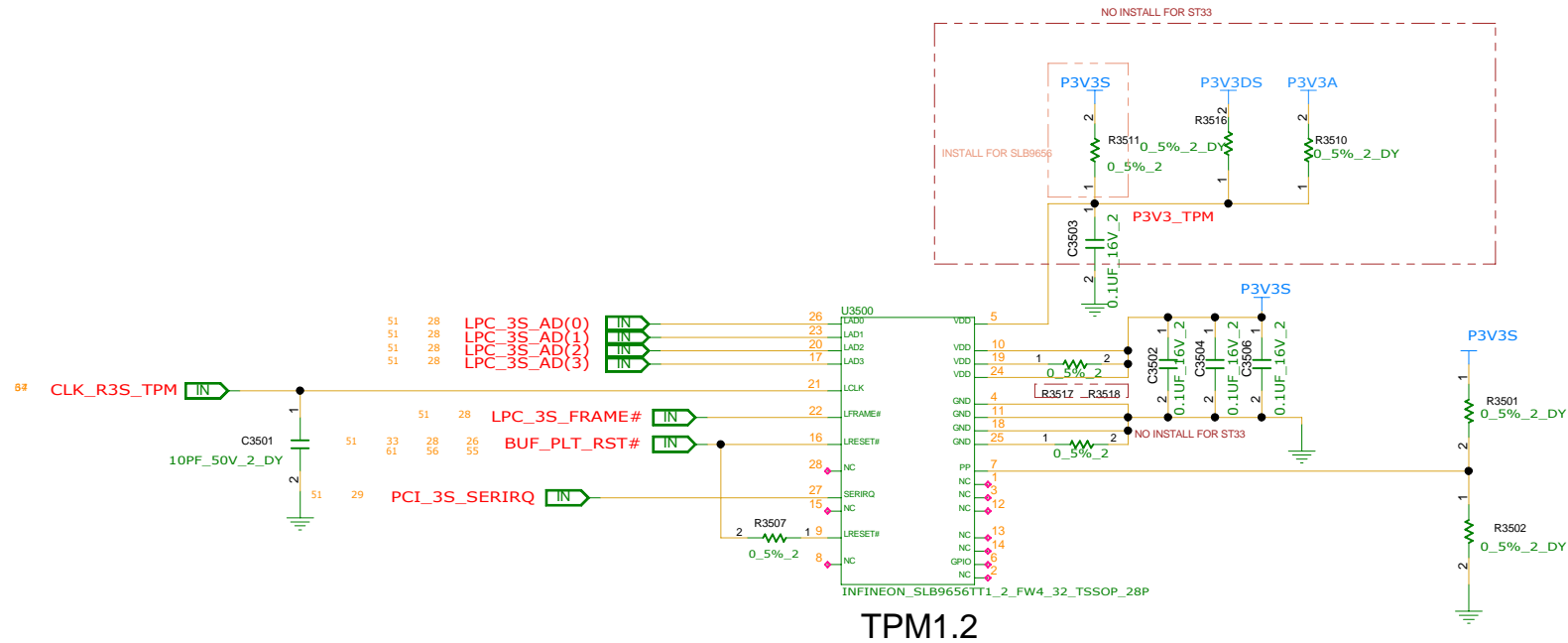
KEYBOARD

DOC NUMBER 1310xxxx-0-0

REV X01

SHEET 52 of 78

CHANGE by XXX DATE 21-OCT-2002



SLB9656TT1.2		ST33
C3503	INSTALL	OPEN
R3507	INSTALL	OPEN
R3511	INSTALL	OPEN
R3517	INSTALL	OPEN
R3518	INSTALL	OPEN

REFERENCE NUMER : 3500~3549

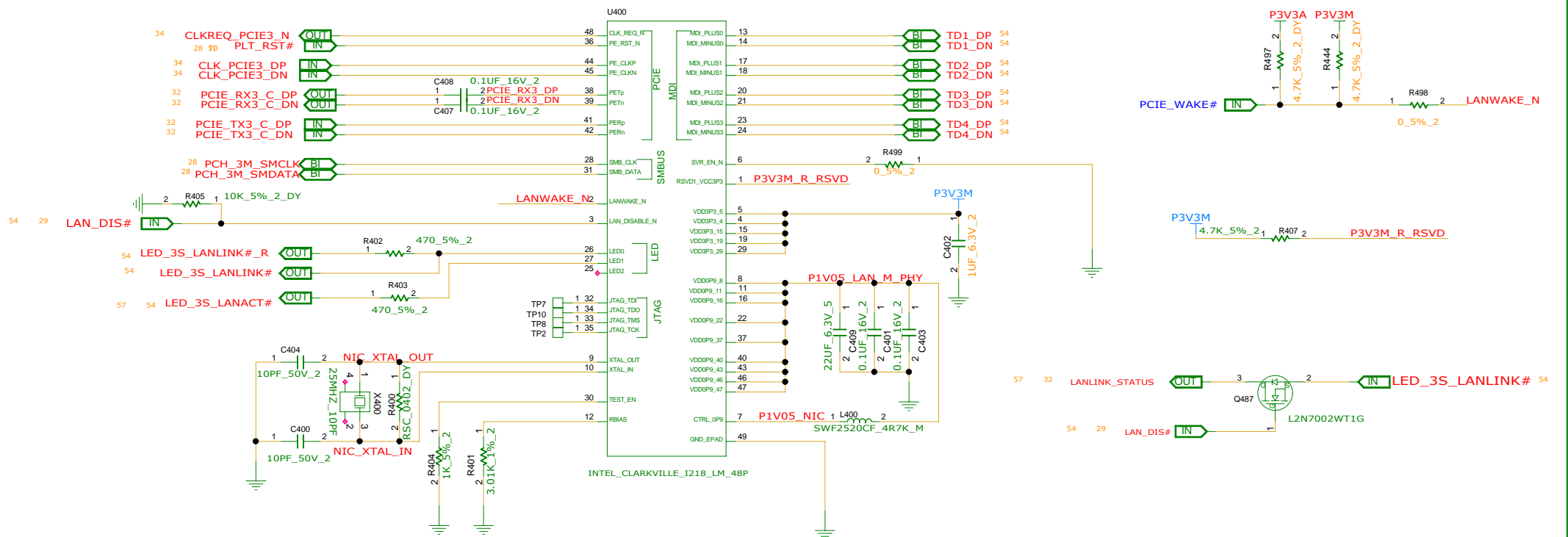
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
TPM

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

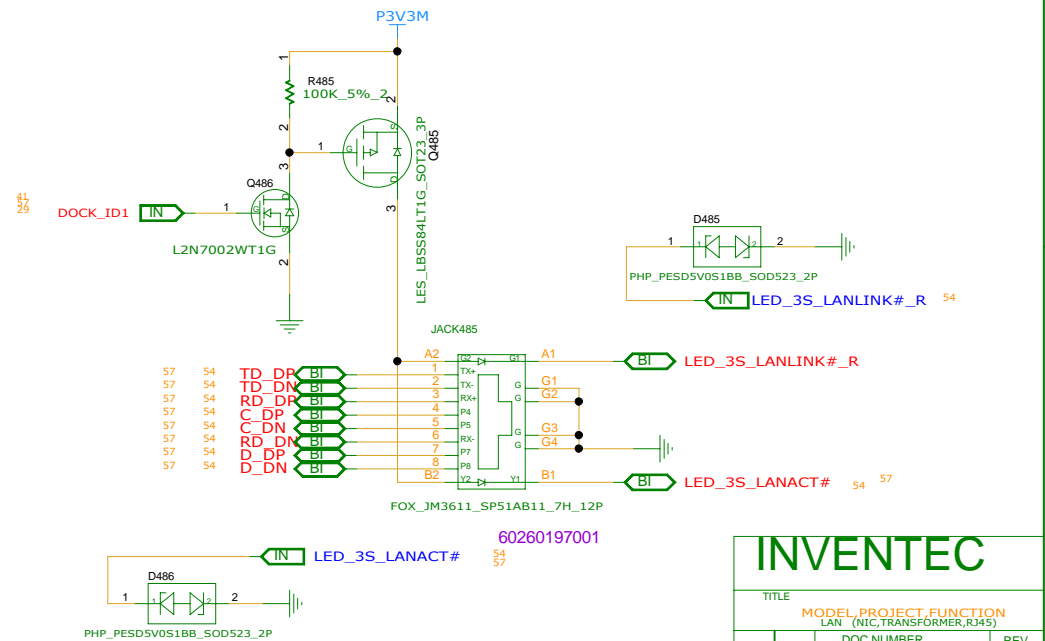
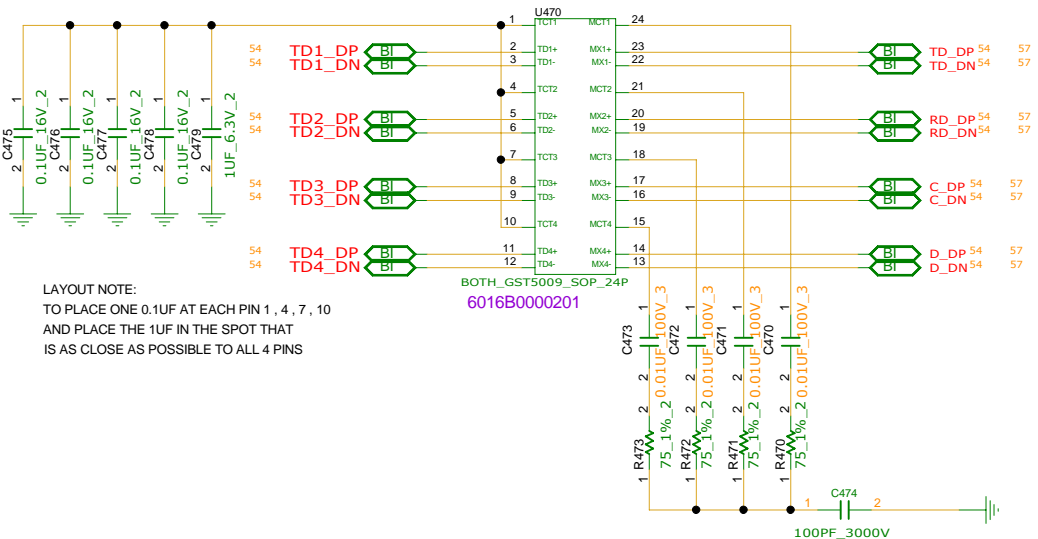
CHANGE by XXX DATE 21-OCT-2002

SHEET 53 of 78



REFERENCE NUMER : 400~469

REFERENCE NUMER : 470~499



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
LAN (NIC, TRANSFORMER, RJ45)			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
A3	CS	54 of 78	



A

CHANGE by	XXX	DATE	21-OCT-2002
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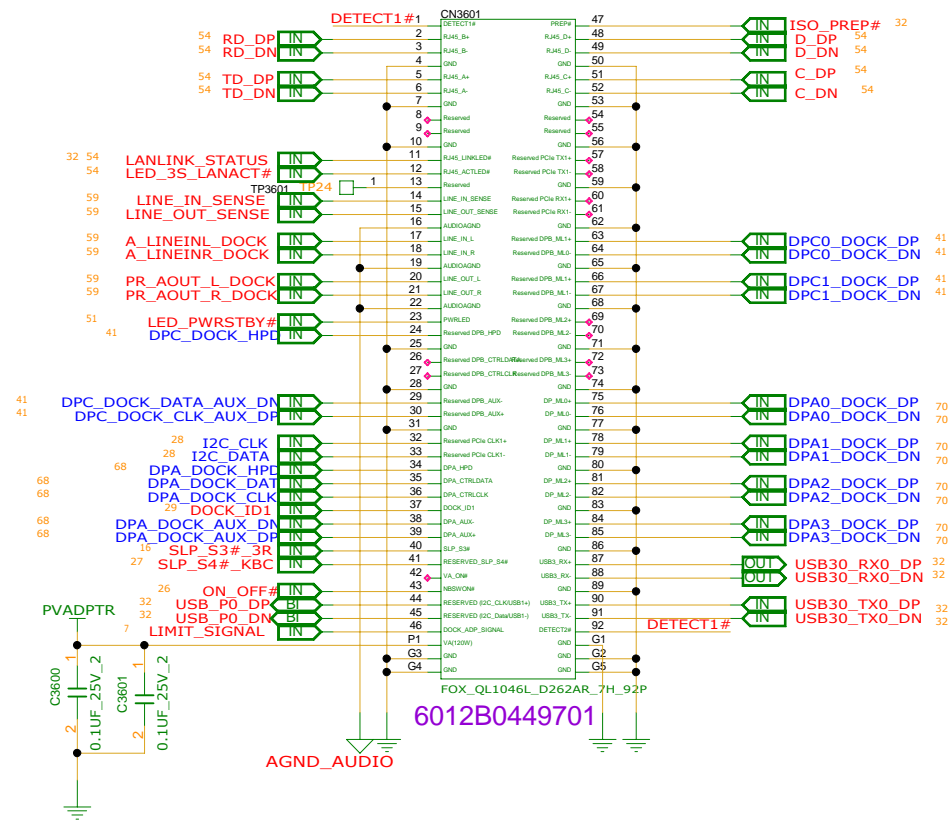
INVENTEC

TITLE			MODEL,PROJECT,FUNCTION WLAN & BLUE TOOTH		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0			
		SHEET 55 of 78			

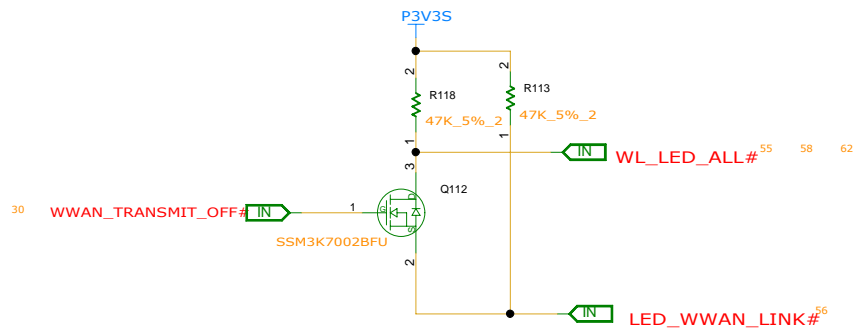


6026B0243101
KEY B TYPE
NGFF WWAN

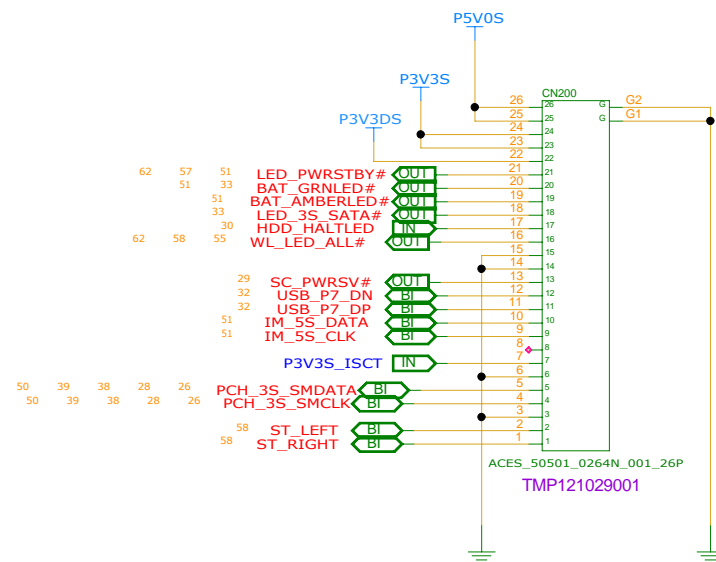
CHANGE by	XXX	DATE	21-OCT-2002
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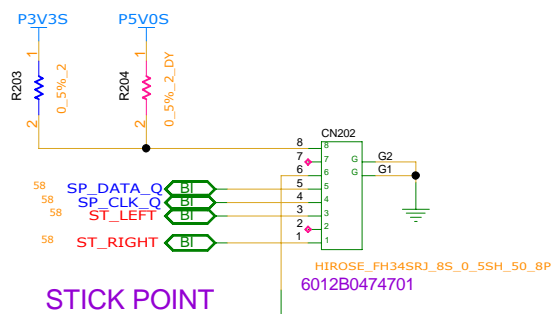
INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
DOCKING CONN.			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 57 of 78			



WLAN_WWAN_BLUETOOTH_LED

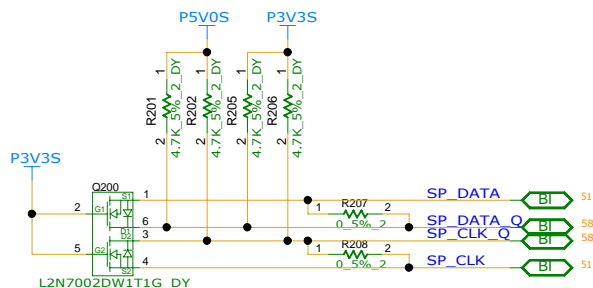


SMART CARD AND TOUCHPAD D/B W TO B CONN



STICK POINT

R201	5V	3.3V
R202	INSTALL	UNINSTALL
R203	UNINSTALL	INSTALL
R204	INSTALL	UNINSTALL

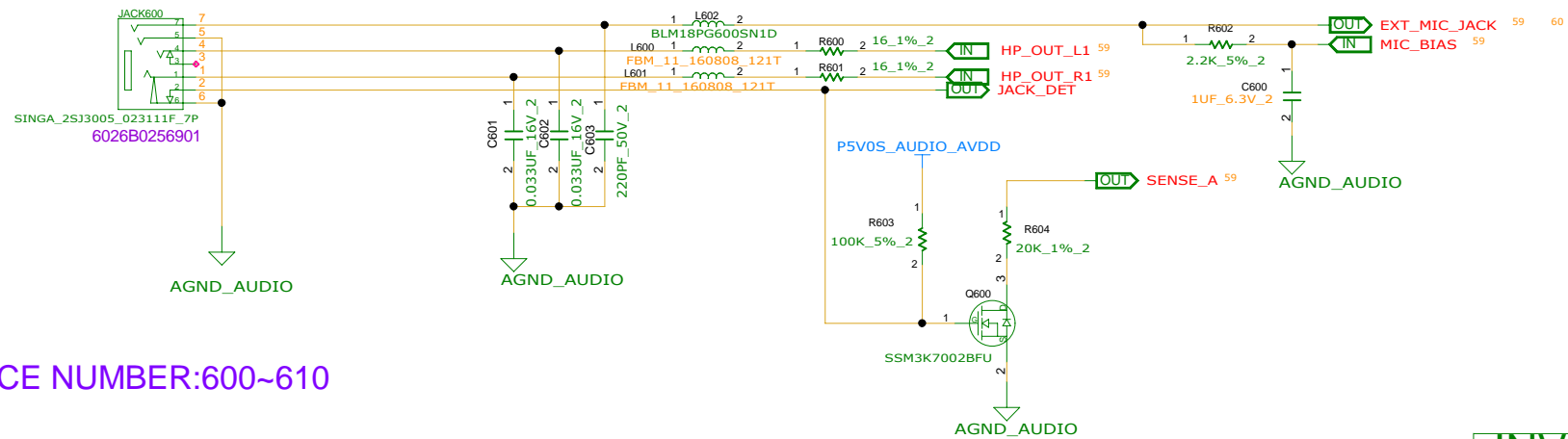
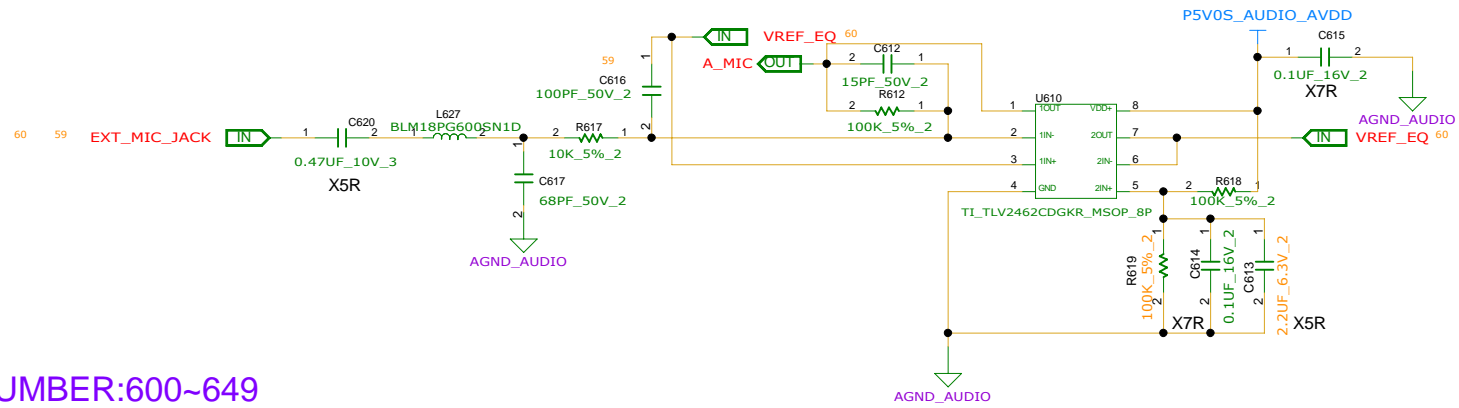


STICK POINT OPTION

REFERENCE NUMBER:100~199

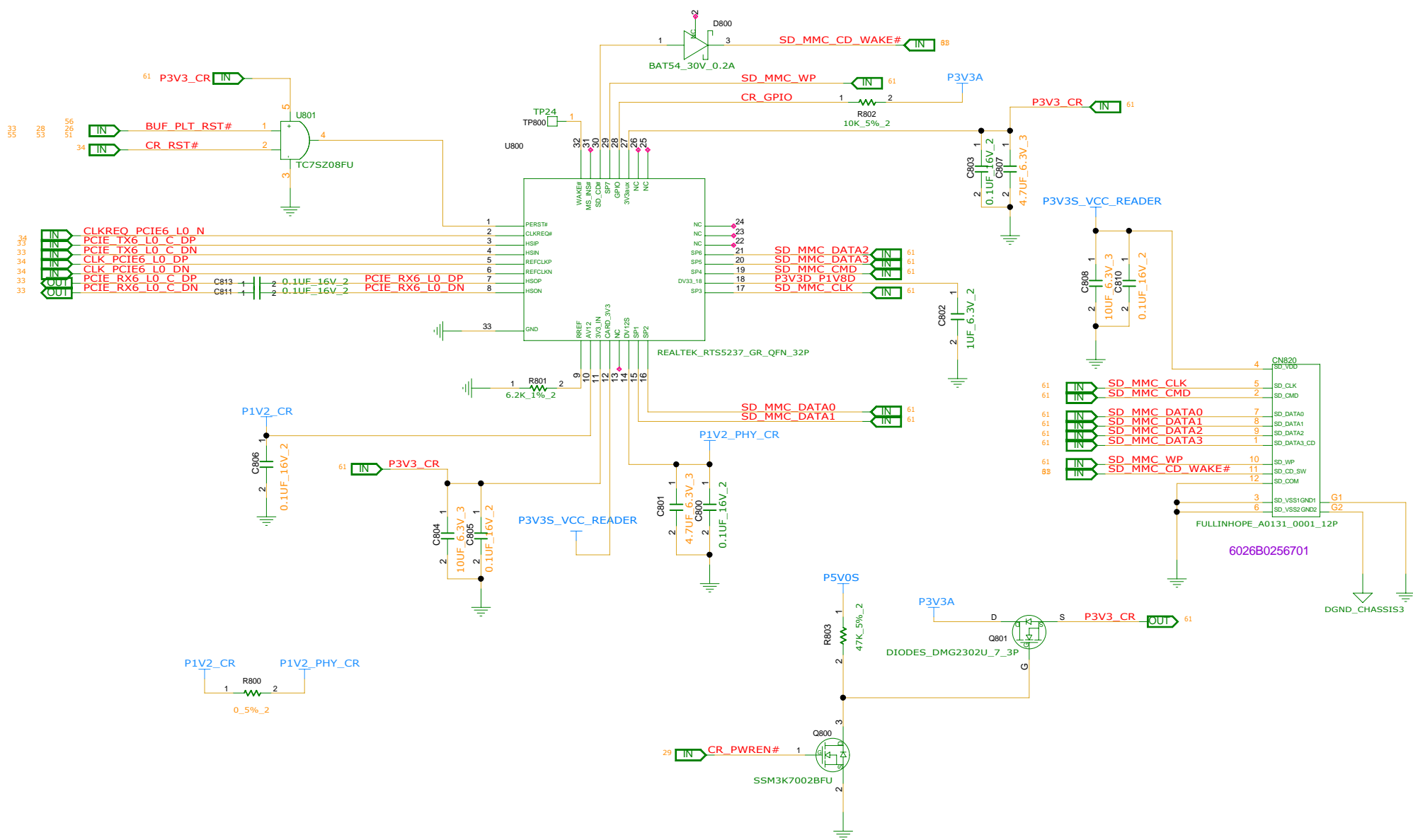
INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
STICK POINT & B2B CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 58 of 78			





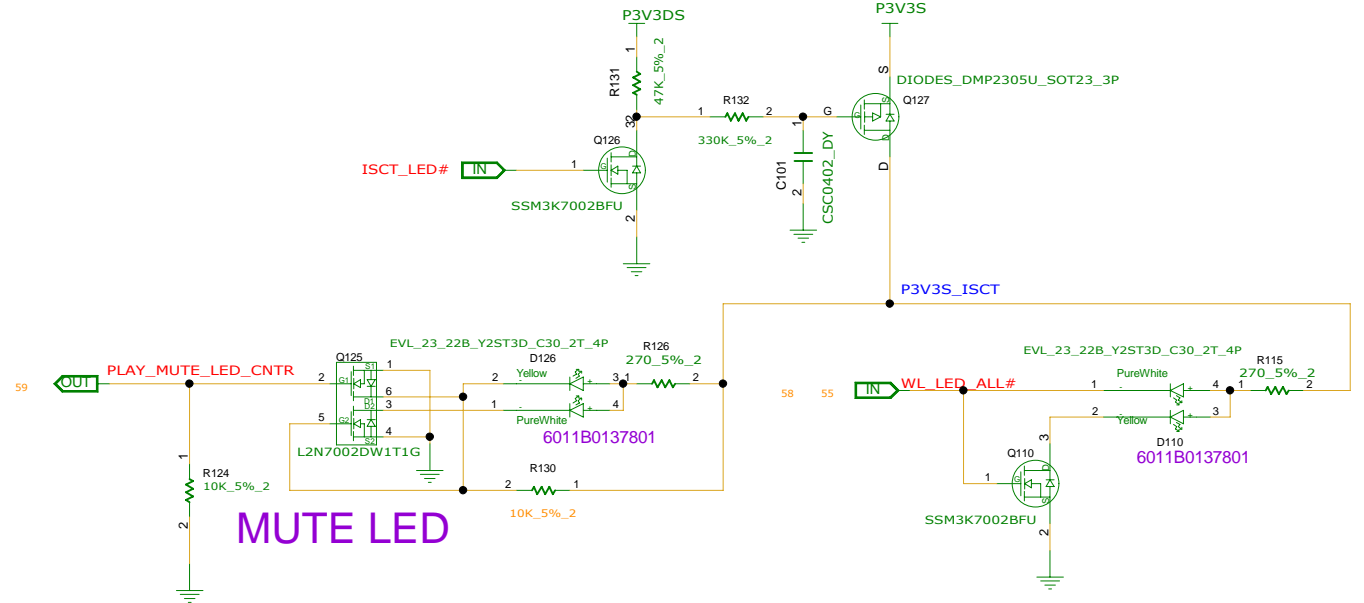
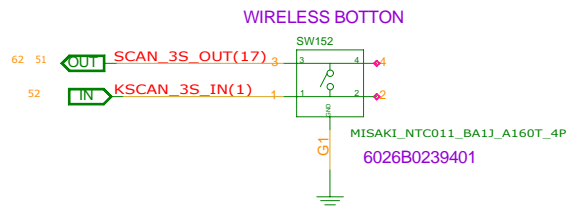
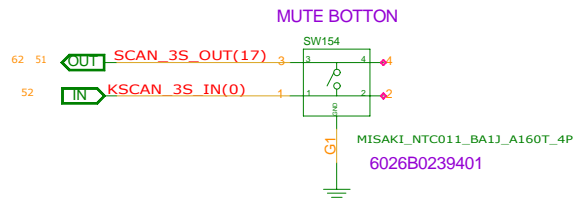
INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION		
EXT. MIC AMP. & AUDIO JACK		
SIZE A3	CODE CS	REV X01
DOC NUMBER 1310xxxxx-0-0		
SHEET 60 of 78		



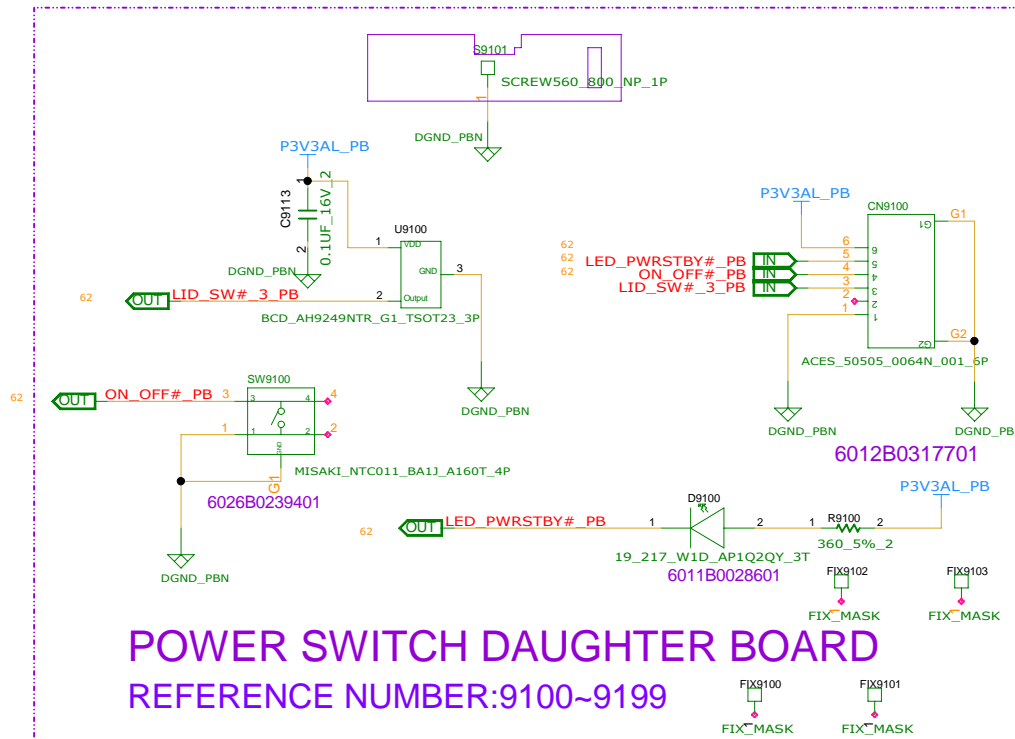
REFERENCE NUMBER:800~899

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 61 of 78			

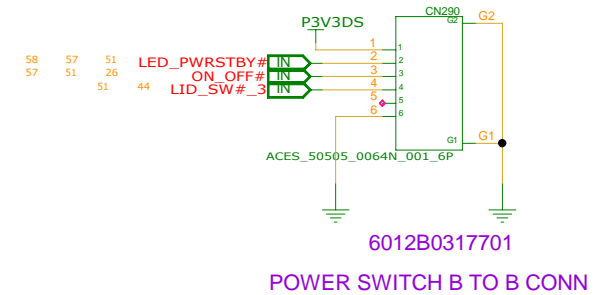


MUTE LED

WIRELESS/BLUETOOTH LED

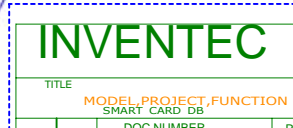


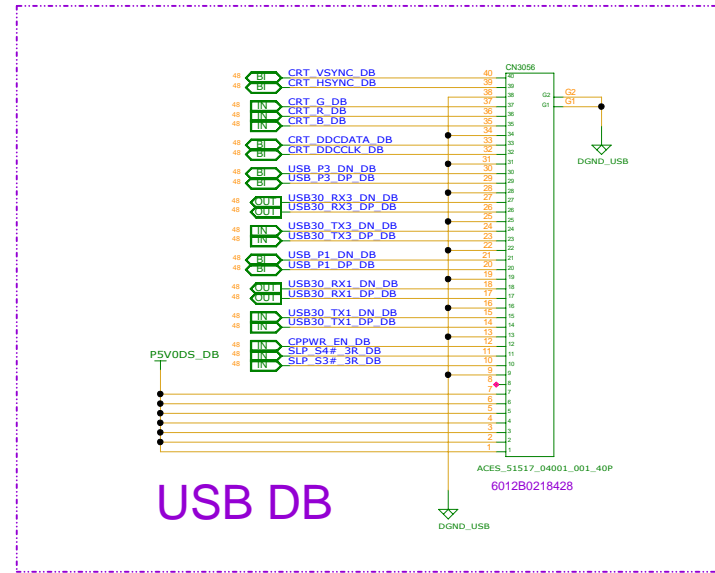
POWER SWITCH DAUGHTER BOARD
REFERENCE NUMBER:9100~9199



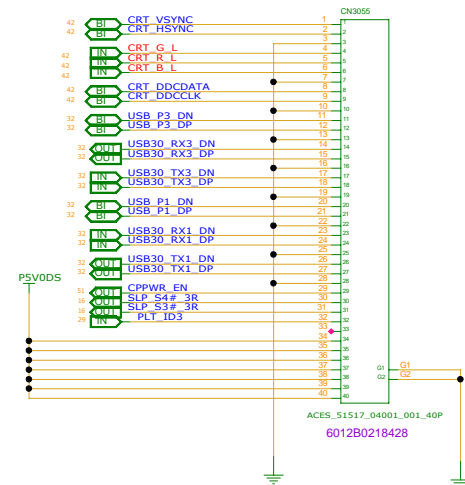
6012B0317701
POWER SWITCH B TO B CONN

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
SIZE	CODE	DOC.NUMBER	REV
A3	C3	1310xxxxx-0-0	X01
SHEET 62 of 78			

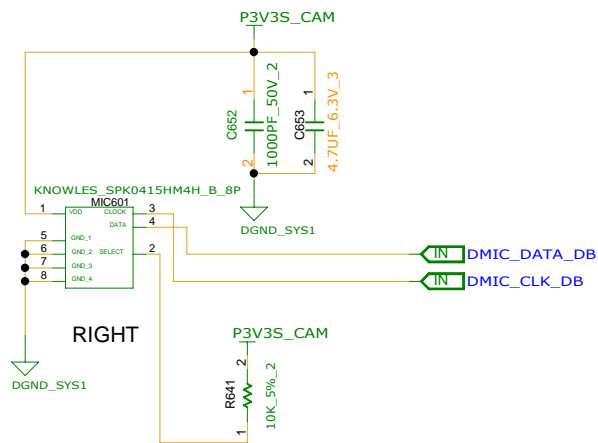
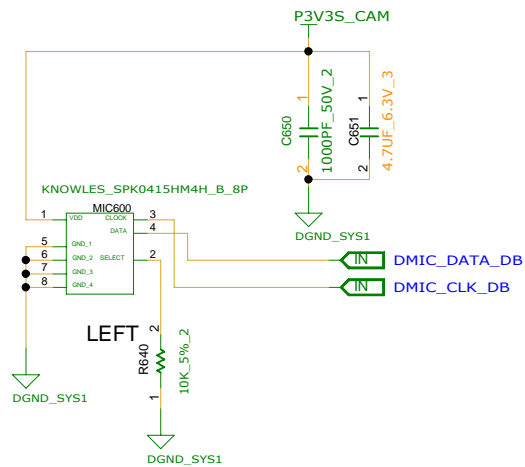
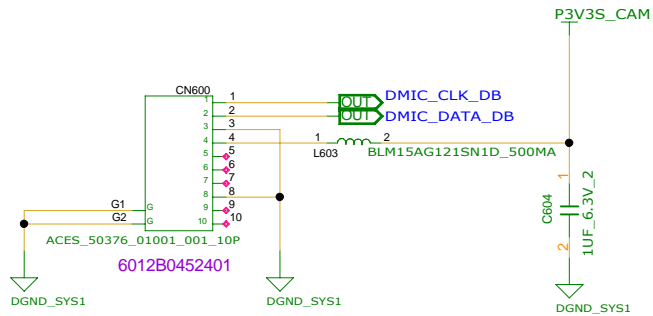




USB DB



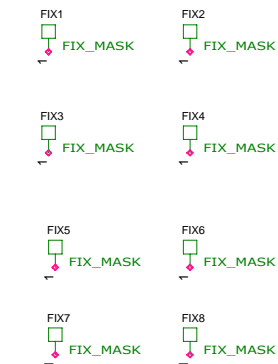
INVENTEC			
MODEL,PROJECT,FUNCTION			
RESERVE			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XXXX-0-0	201
SHEET		64	of 68



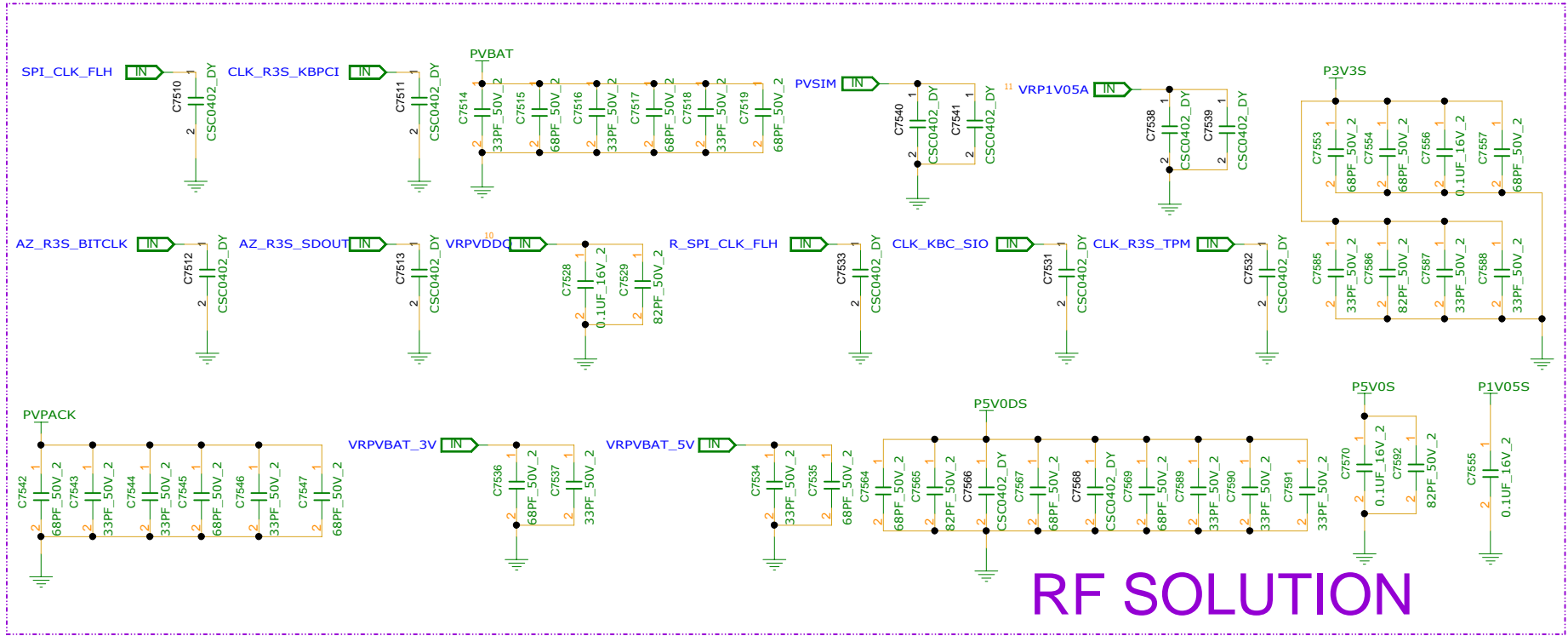
REFERENCE NUMBER:600-649
MIC DOUGHTER BOARD

INVENTEC

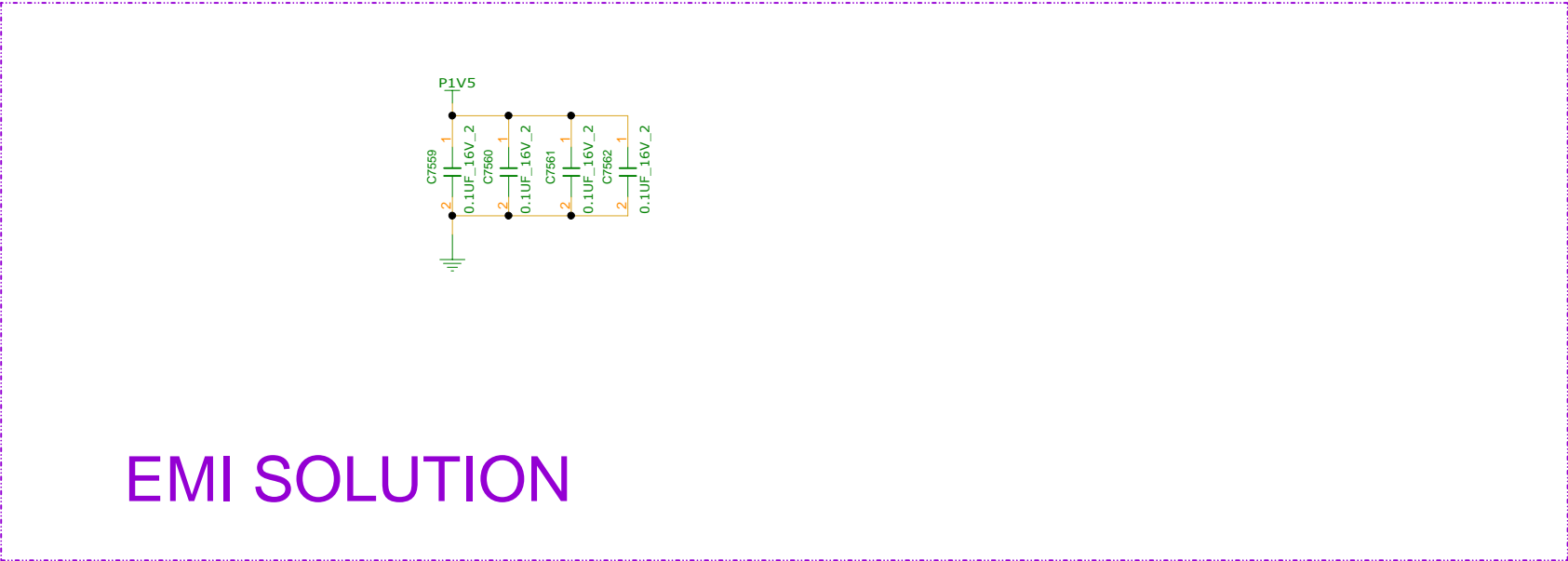
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 65 of 78			



SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	REV X01
SHEET 66 of 78			



RF SOLUTION



EMI SOLUTION

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
EMI & RF SOLUTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 67 of 78			

MLPS TABLE

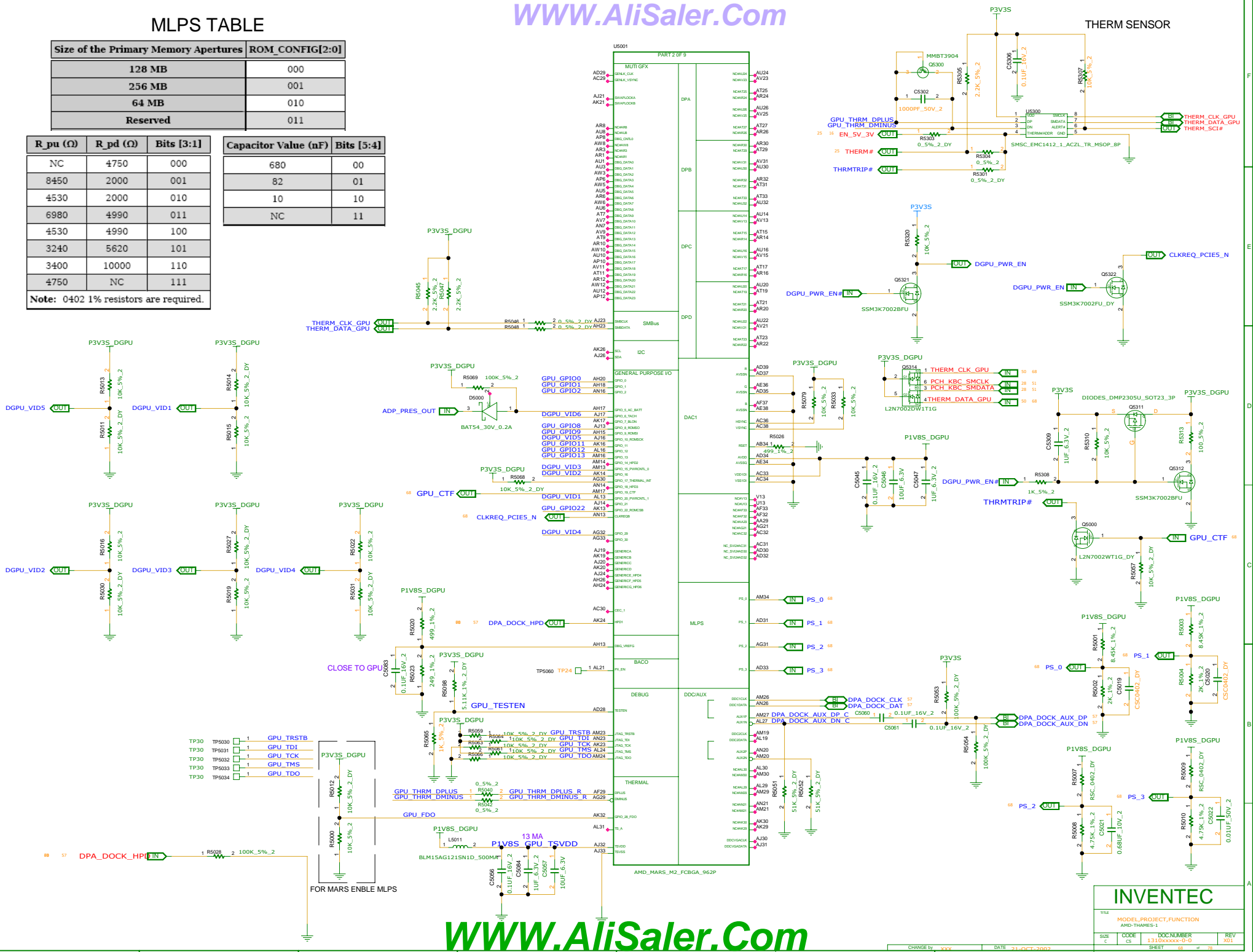
Size of the Primary Memory Apertures	ROM_CONFIG[2:0]
128 MB	000
256 MB	001
64 MB	010
Reserved	011

R _{pu} (Ω)	R _{pd} (Ω)	Bits [3:1]
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111

Note: 0402 1% resistors are required.

Capacitor Value (nF)	Bits [5:4]
680	00
82	01
10	10
NC	11

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INVENTEC

TITLE	MODEL,PROJECT,FUNCTION
AMD-THAMES-1	
SIZE	CODE
131000000-0-0	DOCNUMBER
SHEET	REV
68	001

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CHANGE BY: VXX DATE: 21-OCT-2002

LPT-LP GPIO 34	MARS MLPS Bit: PS_3 [3:1]			R _{pu} (Ω)	R _{pd} (Ω)	Vendor & PN	Die Ver.	
0	0	0	0	NC	4750	Samsung - K4G20325FD-FC04	D	GDDR5 - 64Mx32/128Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
	0	0	1	8450	2000	Hynix - H5GQ2H24AFR-T2C	A (Gemma)	GDDR5 - 64Mx32/128Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
1	1	1	0	3400	10000	*Samsung - K4G41325FC-HC04	C	*GDDR5 - 128Mx32/256Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
	1	1	1	4759	NC	*Hynix - H5GC4H24MFR-T0C	Huma	*GDDR5 - 128Mx32/256Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
VBIOS selection :	Vram configuration			Vendor ID		Vram information		
0 : VBIOS 1, 64Mx32 for 1GB sku	00: 64Mx32 (2Gb)			0: Samsung		* 2GB sku, TBD		
1 : VBIOS 2, 128Mx32 for 2GB sku	11: 128Mx32 (4Gb)			1: Hynix				
				Resistor Divider				
				Lookup Table				

MLPS Implementation

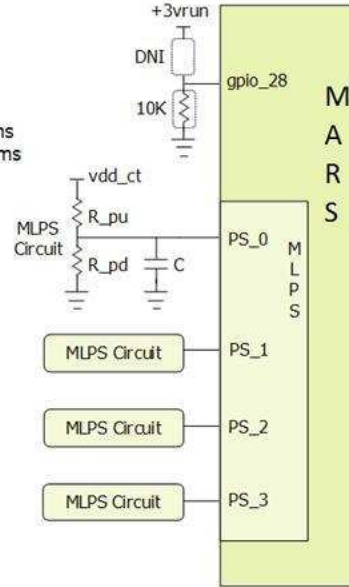
- Connect GPIO_28 to 10K pulldown to enable MLPS
- If any of PS_0/1/2/3 is not used, leave "no connect"
- R_{pu}, R_{pd} and C must be properly populated per tables below
- Place MLPS circuit components as close to the ASIC as possible
- Total DC resistance of trace between PS pin and C should be less than 2 ohms
- Total DC resistance of trace between C and ground should be less than 2 ohms
- Trace capacitance should be less than 100pF. Resistors should be of +/-1% tolerance

Capacitor Lookup Table

C (nF)	Bits(5,4)
680	00
82	01
10	10
NC	11

Resistor Divider Lookup Table

R _{pu} (Ohm)	R _{pd} (Ohm)	Bits(3,2,1)
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111



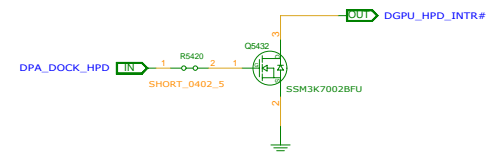
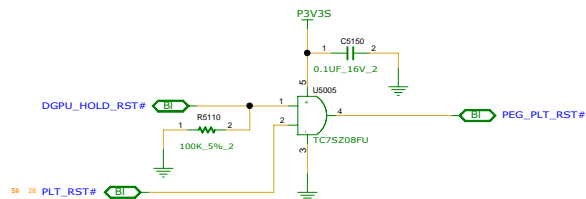
MLPS Bit	Strap Name	Description
PS_0[1]	ROM_CONFIG[0]	See Primary Memory Aperture Sizes.
PS_0[2]	ROM_CONFIG[1]	
PS_0[3]	ROM_CONFIG[2]	
PS_0[4]	N/A	Reserved for internal use only. Must be 1 at reset.
PS_0[5]	AUD_PORT_CONN_PINSTRAP[0]	The LSB (least significant bit) of the strap that indicates the number of audio-capable display outputs.
PS_1[1]	STRAP_BIF_GEN3_EN_A	PCIe GEN3 capability. 1 = PCIe GEN3 is supported.
PS_1[2]	STRAP_BIF_CLK_PM_EN	Determines whether or not the PCIe reference clock power management capability is reported in the PCI configuration space (otherwise known as CLKREQB). 0 = The CLKREQB power management capability is disabled
PS_1[3]	N/A	Reserved for internal use only. Must be 0 at reset.
PS_1[4]	STRAP_TX_CFG_DRV_FULL_SWING	Control the transmitter full-/half-swing mode 1 = The transmitter full-swing is enabled
PS_1[5]	STRAP_TX_DEEMPH_EN	PCI EXPRESS® transmitter, de-emphasis enable. 1 = Tx deemphasis enabled.
PS_2[1]	N/A	Reserved.
PS_2[2]	N/A	Reserved.
PS_2[3]	STRAP_BIOS_ROM_EN	To enable the external BIOS ROM device. 0 = Disable the external BIOS ROM device.
PS_2[4]	STRAP_BIF_VGA_DIS	VGA disable determines whether or not the card will be recognized as the system's VGA controller (through the SUBCLASS field in the PCI configuration space). 0 = VGA controller capacity enabled.
PS_2[5]	N/A	Reserved.
PS_2[1]	N/A	Reserved.
PS_2[2]	N/A	Reserved.
PS_2[3]	STRAP_BIOS_ROM_EN	To enable the external BIOS ROM device. 0 = Disable the external BIOS ROM device.
PS_2[4]	STRAP_BIF_VGA_DIS	VGA disable determines whether or not the card will be recognized as the system's VGA controller (through the SUBCLASS field in the PCI configuration space). 0 = VGA controller capacity enabled.
PS_2[5]	N/A	Reserved.
PS_3[1]	BOARD_CONFIG[0]	See Board configuration related strapping, such as for memory ID.
PS_3[2]	BOARD_CONFIG[1]	
PS_3[3]	BOARD_CONFIG[2]	
PS_3[4]	AUD_PORT_CONN_PINSTRAP[1]	Determines the maximum number of digital display audio endpoints that will be presented to the OS and user. 111 = No usable endpoints. 110 = One usable endpoint. 101 = Two usable endpoints. 100 = Three usable endpoints. 011 = Four usable endpoints. 010 = Five usable endpoints. 001 = Six usable endpoints. 000 = All endpoints are usable.
PS_3[5]	AUD_PORT_CONN_PINSTRAP[2]	

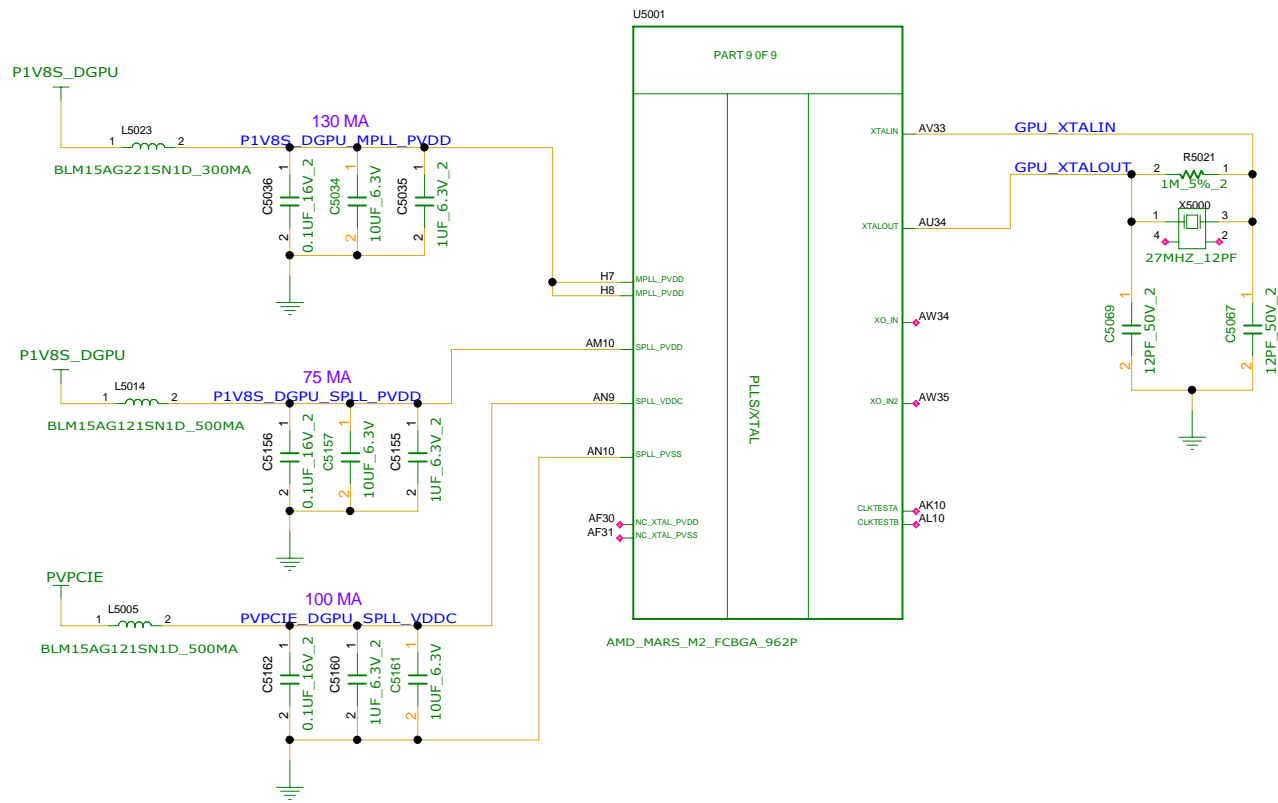
Note : AUD[1] (on HYSNC) and AUD[0] (on VSYNC) still need to be properly pin strapped even in a MLPS-based design.

Pin/Bit	Name	Description	Default	Legacy
PS_0[3:1]	romidcfg[2:0]	Memory aperture size or ROM type select: If bios_rom_en = 0, romidcfg[2:0] define memory aperture size If bios_rom_en = 1, romidcfg[2:0] define ROM type	xxx	gpio_13 gpio_12 gpio_11
PS_0[4]	n/a	Reserved	1	genlk_vsync
PS_1[1]	bif_gen3_en_a	PCIe Gen3 capability: 1=Gen3 supported, 0=Gen3 not supported	x	gpio_2
PS_1[2]	bif_clk_pm_en	PCIe Clk PM capability: 1 = CLKREQB supported	x	gpio_8
PS_1[3]	n/a	Reserved		genlk_clk
PS_1[4]	tx_pwrs_enb	PCIe Tx power savings: 0=50% swing, 1=full swing	x	gpio_0
PS_1[5]	tx_deemph_en	PCIe Tx de-emphasis: 1=Tx de-emphasis enabled	x	gpio_1
PS_2[1]	n/a	Reserved		n/a
PS_2[2]	n/a	Reserved		n/a
PS_2[3]	bios_rom_en	Enable external BIOS ROM: 1=External ROM connected	x	gpio_22
PS_2[4]	vga_dis	VGA disable: 1=Disable this GPU as the system's VGA controller	0	gpio_9
PS_2[5]	n/a	Reserved		n/a
PS_3[1]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[2]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[3]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[5]	aud_port_cp[2] aud_port_cp[1] aud_port_cp[0]	3-bit field indicating number of audio-capable display outputs	xxx	n/a

INVENTEC

Title			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XXXX-0-0	201
SHEET		of	08





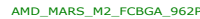
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 71 of 78			

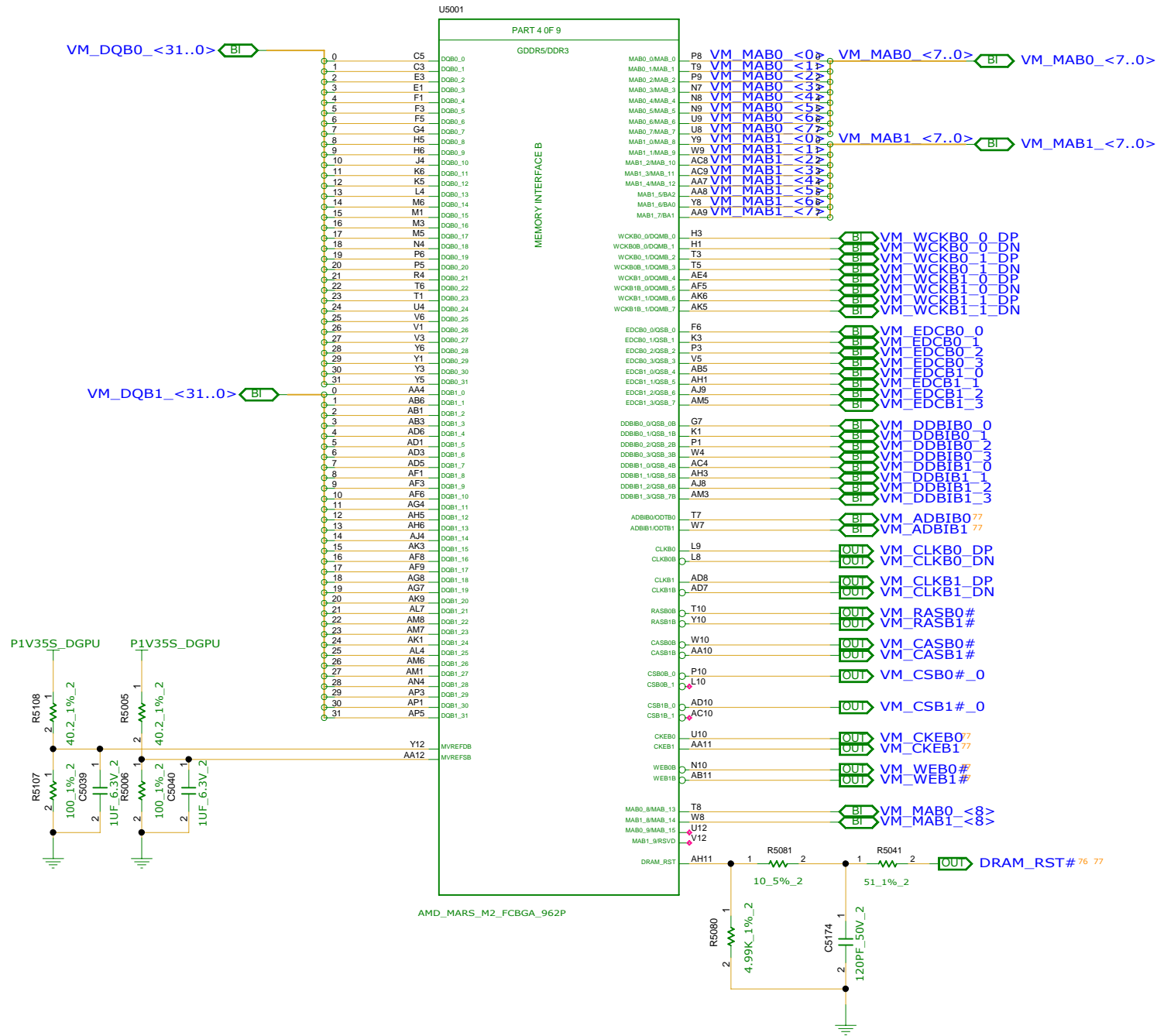




CHANGE by	YYY	DATE	31-OCT-2002
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SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
		SHEET 74 of 78	

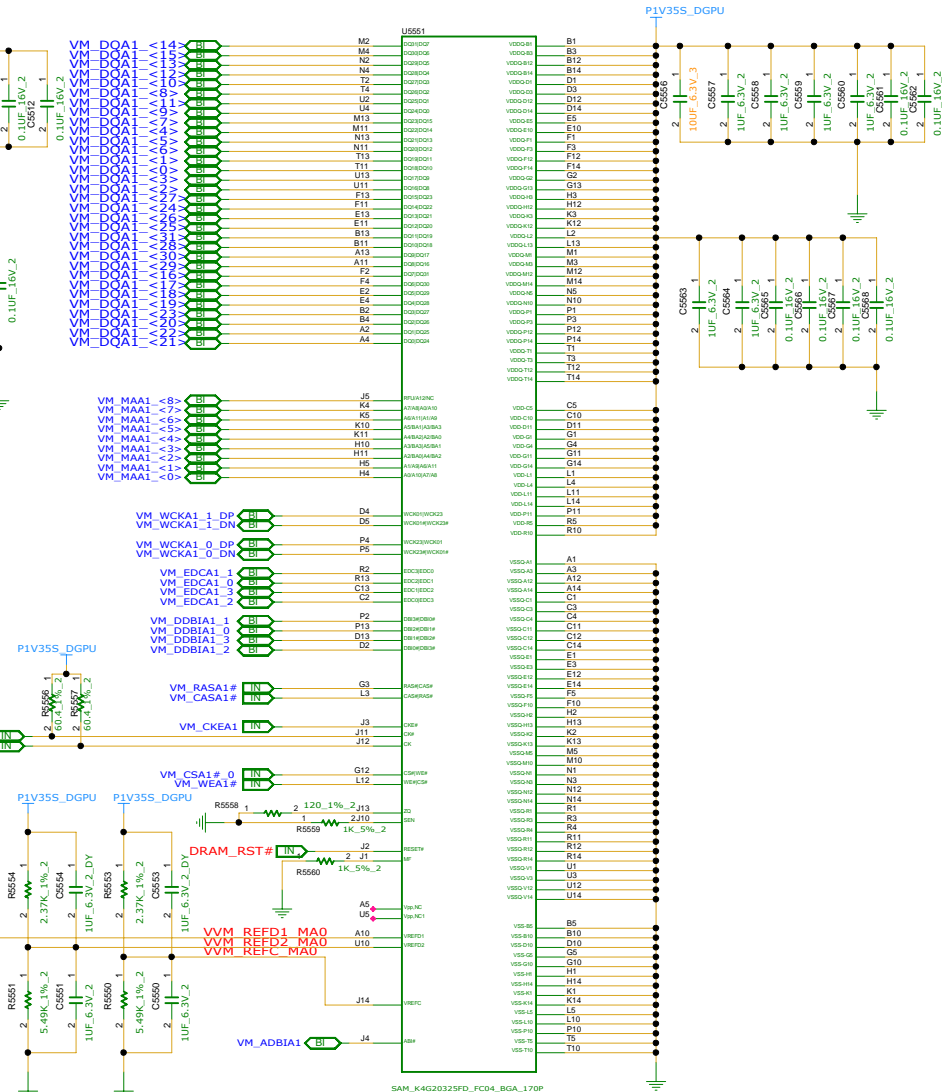
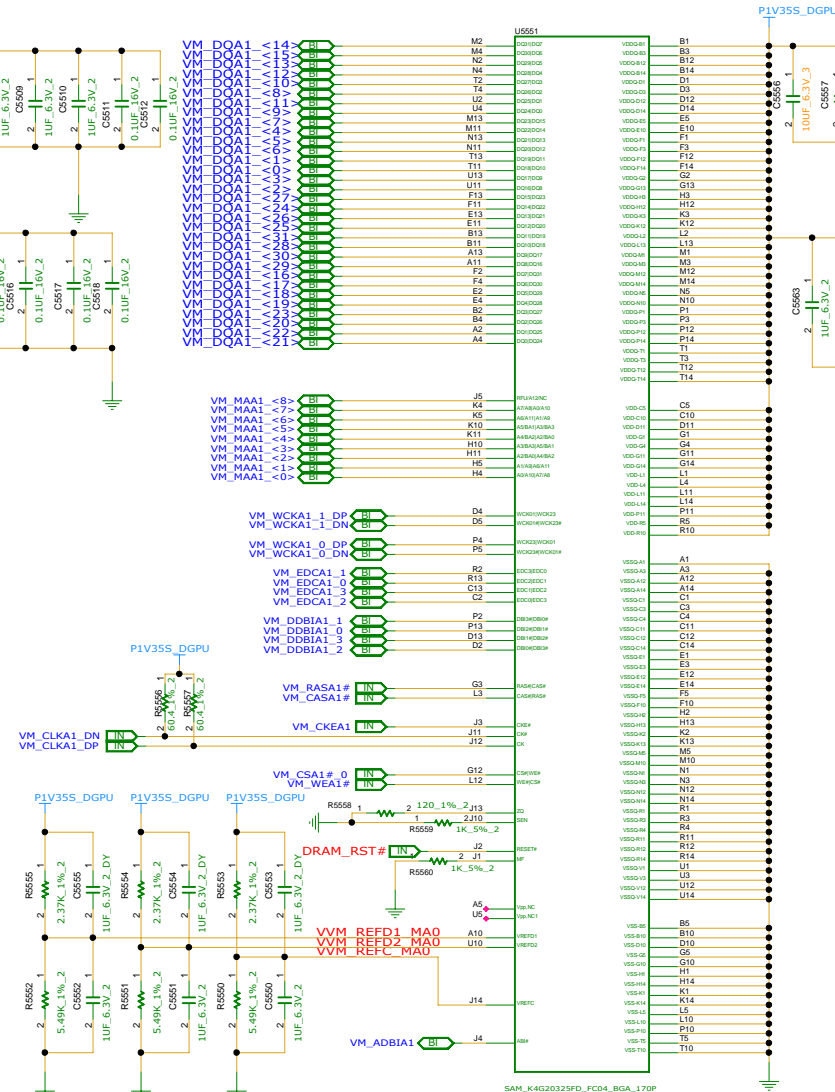


INVENTEC

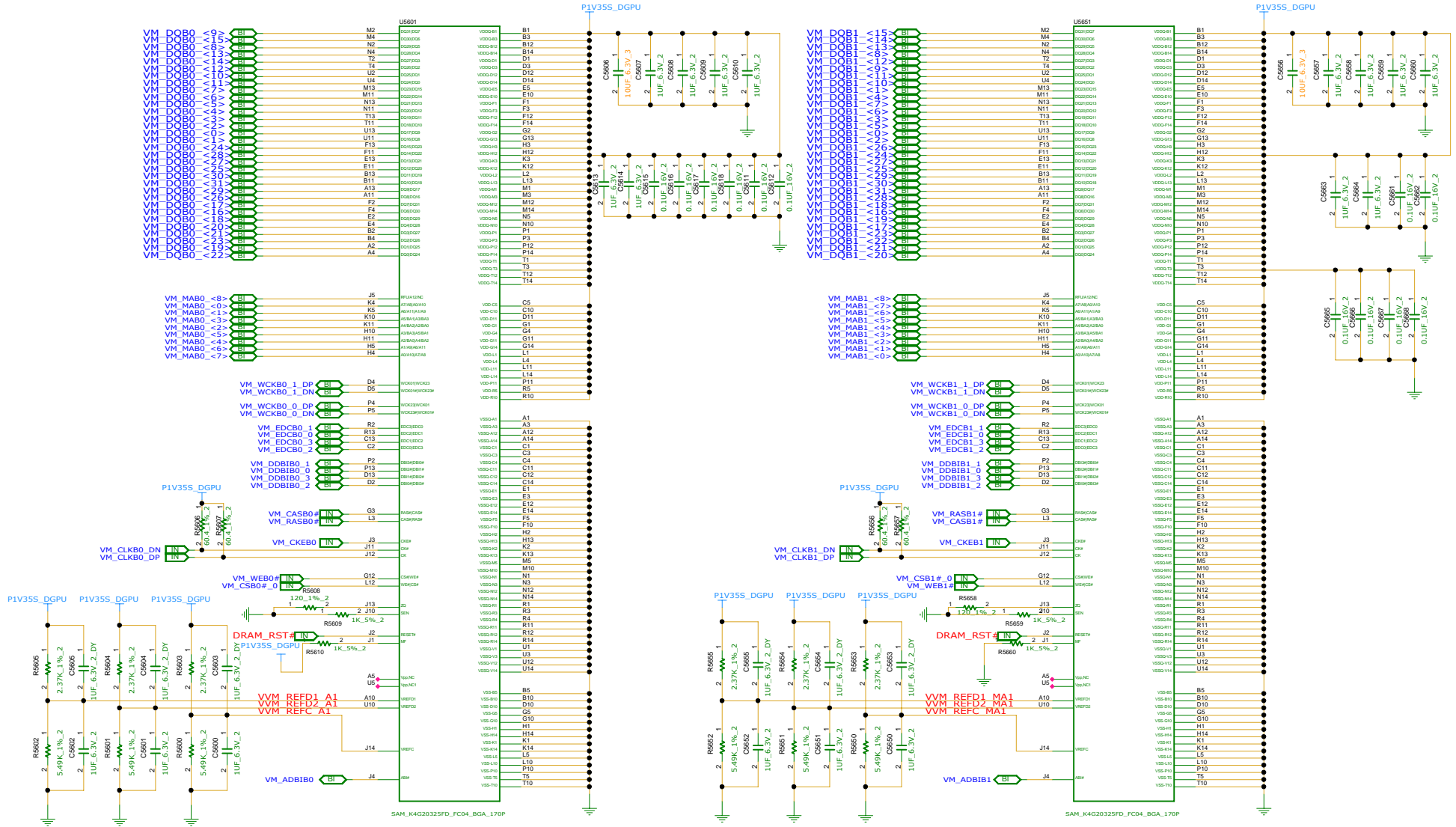
TITLE
MODEL,PROJECT,FUNCTIONSIZE A3
CODE CS
DOC NUMBER
1310xxxxx-0-0
REV
X01

SHEET 75 of 78

CHANGE by XXX DATE 21-OCT-2002



TITLE			
MODEL,PROJECT,FUNCTION VRAM-1			
SIZE C	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 76 of 78			



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
VRAN-2			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310VANKK-0-0	201
SHEET		27	of 28

8	7	6	5	4	3	2	1
D							
C							
B							
A							
8	7	6	5	4	3	2	1

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 78 of 78			